



P-Channel 12 V (D-S) MOSFET

MOSFET PRODUCT SUMMARY						
V _{DS} (V)	$R_{DS(on)}\left(\Omega\right)Max.$	I _D (A) ^a	Q _g (Typ.)			
	0.028 at V _{GS} = - 4.5 V	- 6 ^e				
	0.032 at V _{GS} = - 3.7 V	- 6 ^e				
- 12	0.040 at V _{GS} = - 2.5 V	- 6 ^e	9 nC			
	0.063 at V _{GS} = - 1.8 V	- 4.5				
	0.150 at V _{GS} = - 1.5 V	- 3.6				

FEATURES

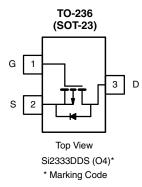
- TrenchFET® Power MOSFET
- 100 % R_g Tested
- Material categorization: For definitions of compliance please see www.vishav.com/doc?99912



HALOGEN FREE

APPLICATIONS

- Smart Phones and Tablet PCs
 - Load Switch
 - Battery Switch



Ordering Information: Si2333DDS-T1-GE3 (Lead (Pb)-free and Halogen-free)

ABSOLUTE MAXIMUM RATINGS $(T_A =$	25 °C, unless otl	herwise noted)		
Parameter	Symbol	Limit	Unit	
Drain-Source Voltage	V _{DS}	- 12	V	
Gate-Source Voltage	V_{GS}	± 8	"	
	T _C = 25 °C		- 6 ^e	
Continuous Drain Current (T _{.1} = 150 °C)	T _C = 70 °C	I _D	- 5.2	
Continuous Diam Current (1) = 130 °C)	T _A = 25 °C		- 5 ^{b, c}	
	T _A = 70 °C		- 4 ^{b, c}	A
Pulsed Drain Current (t = 300 μs)	I _{DM}	- 20		
Continuous Source-Drain Diode Current	T _C = 25 °C	Is	- 1.4	
Continuous Source-Diain Diode Current	T _A = 25 °C	'S	- 0.63 ^{b, c}	
	T _C = 25 °C		1.7	
Maximum Power Dissipation	T _C = 70 °C	P_D	1.1	_ w
Maximum rower bissipation	T _A = 25 °C	. D	1.20 ^{b, c}	
	T _A = 70 °C		0.6 ^{b, c}	
Operating Junction and Storage Temperature Range	T _J , T _{stg}	- 55 to 150	°C	

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient ^{b, d}	≤ 5 s	R _{thJA}	100	130	°C/W	
Maximum Junction-to-Foot (Drain)	Steady State	R_{thJF}	60	75] 0/**	

- a. Based on T_C = 25 °C.
- b. Surface mounted on 1" x 1" FR4 board.
- c. t = 5 s.
- d. Maximum under steady state conditions is 175 °C/W.
- e. Package limited.

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MOSFET SPECIFICATIONS (T _J = 25 °C, unless otherwise noted)							
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static	T					1	
Drain-Source Breakdown Voltage	V_{DS}	$V_{DS} = 0 \text{ V}, I_{D} = -250 \mu\text{A}$	- 12			V	
V _{DS} Temperature Coefficient	$\Delta V_{DS}/T_{J}$	I _D = - 250 μΑ		- 8		mV/°C	
V _{GS(th)} Temperature Coefficient	$\Delta V_{GS(th)}/T_J$			2.4			
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = -250 \mu A$	- 0.4		- 1	V	
Gate-Source Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 8 \text{ V}$			± 100	nA	
Zero Gate Voltage Drain Current	lpee	V _{DS} = - 12 V, V _{GS} = 0 V			- 1	μΑ	
Zero Gate Voltage Drain Current	I _{DSS}	V_{DS} = - 12 V, V_{GS} = 0 V, T_{J} = 55 °C			- 10	μΑ	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \le$ - 5 V, V_{GS} = - 4.5 V	- 20			Α	
		V _{GS} = - 4.5 V, I _D = - 5 A		0.023	0.028		
		V _{GS} = - 3.7 V, I _D = - 4.6 A		0.026	0.032	1	
Drain-Source On-State Resistance ^a	R _{DS(on)}	$V_{GS} = -2.5 \text{ V}, I_D = -4.3 \text{ A}$		0.033	0.040	Ω	
		V _{GS} = - 1.8 V, I _D = - 1 A		0.048	0.063		
		V _{GS} = - 1.5 V, I _D = - 0.5 A		0.075	0.150		
Forward Transconductance ^a	9 _{fs}	V _{DS} = - 5 V, I _D = - 5 A		18		S	
Dynamic ^b							
Input Capacitance	C _{iss}			1275			
Output Capacitance	C _{oss}	$V_{DS} = -6 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		255		pF	
Reverse Transfer Capacitance	C _{rss}			236			
Total Cata Charga	Qg	$V_{DS} = -6 \text{ V}, V_{GS} = -8 \text{ V}, I_{D} = -5 \text{ A}$		23	35	nC	
Total Gate Charge				14	21		
Gate-Source Charge	Q_{gs}	$V_{DS} = -6 \text{ V}, V_{GS} = -4.5 \text{ V}, I_{D} = -5 \text{ A}$		2.3			
Gate-Drain Charge	Q_{gd}			3.6			
Gate Resistance	R_{g}	f = 1 MHz	1.9	9.5	19	Ω	
Turn-On Delay Time	t _{d(on)}			26	40		
Rise Time	t _r	V_{DD} = - 6 V, R_L = 6 Ω		24	40		
Turn-Off Delay Time	t _{d(off)}	I_D = - 4 A, V_{GEN} = - 4.5 V, R_G = 1 Ω		45	70	ns	
Fall Time	t _f			20	35		
Drain-Source Body Diode Characteristi	cs						
Continuous Source-Drain Diode Current	I _S	T _C = 25 °C			- 1.4		
Pulse Diode Forward Current ^a	I _{SM}				- 20	A	
Body Diode Voltage	V_{SD}	I _S = - 4 A		- 0.8	- 1.2	V	
Body Diode Reverse Recovery Time	t _{rr}			24	48	ns	
Body Diode Reverse Recovery Charge	Q _{rr}			8	16	nC	
Reverse Recovery Fall Time		$I_F = -4 \text{ A, dI/dt} = 100 \text{ A/}\mu\text{s, T}_J = 25 \text{ °C}$ t_b		9		ns	
Reverse Recovery Rise Time	+			15			

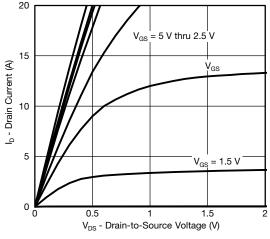
Notes:

- a. Pulse test; pulse width \leq 300 $\mu s,$ duty cycle \leq 2 %.
- b. Guaranteed by design, not subject to production testing.

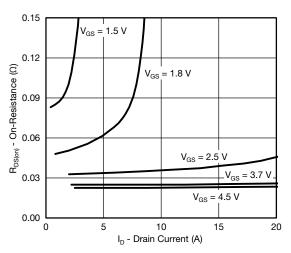
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



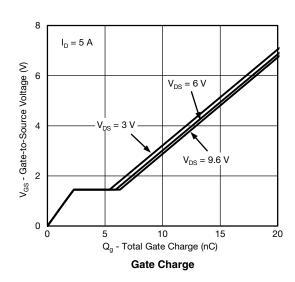
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

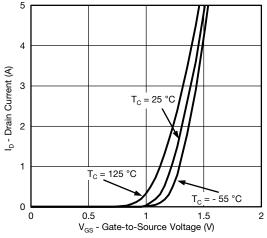




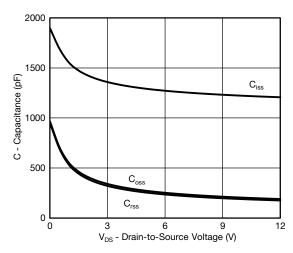


On-Resistance vs. Drain Current and Gate Voltage

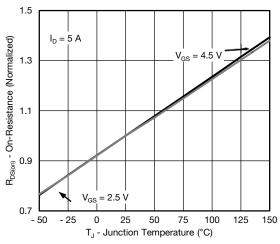




Transfer Characteristics



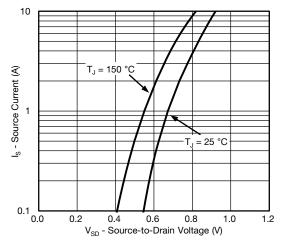
Capacitance



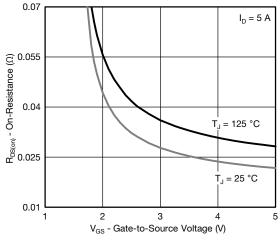
On-Resistance vs. Junction Temperature

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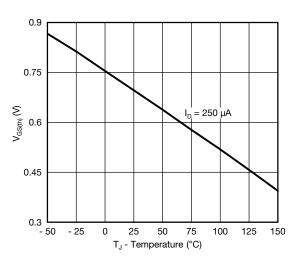
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



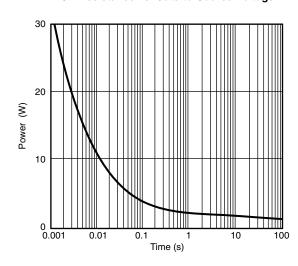
Source-Drain Diode Forward Voltage



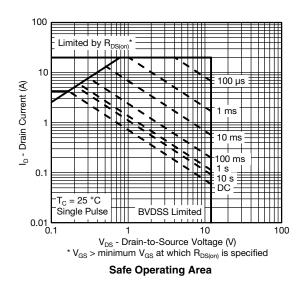
On-Resistance vs. Gate-to-Source Voltage



Threshold Voltage

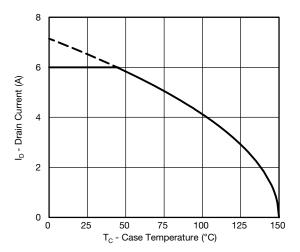


Single Pulse Power

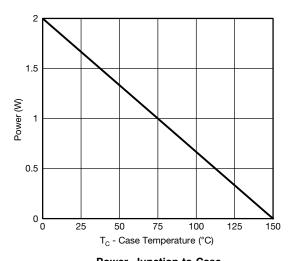


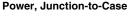


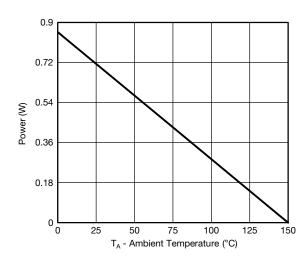
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



Current Derating*





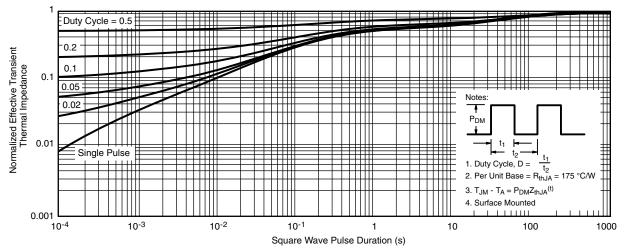


Power, Junction-to-Ambient

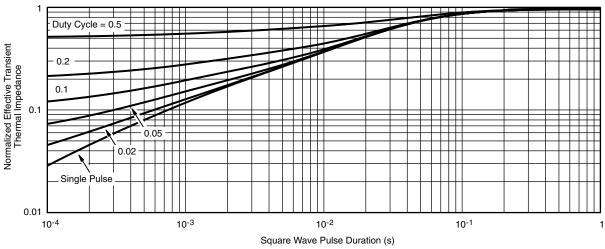
^{*} The power dissipation PD is based on TJ(max.) = 150 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

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TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Ambient

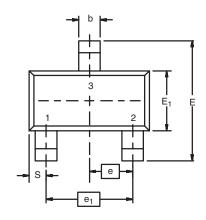


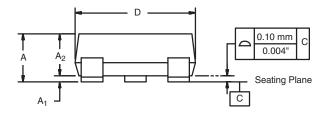
Normalized Thermal Transient Impedance, Junction-to-Foot

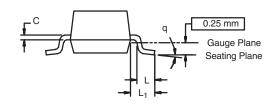
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SOT-23 (TO-236): 3-LEAD







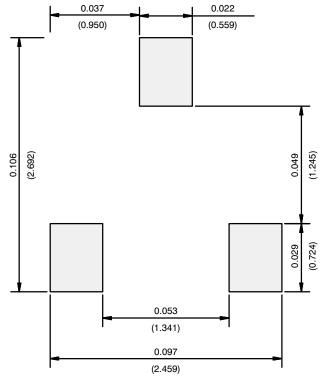
Dim	MILLIMETERS		INCHES		
	Min	Max	Min	Max	
Α	0.89	1.12	0.035	0.044	
A ₁	0.01	0.10	0.0004	0.004	
A ₂	0.88	1.02	0.0346	0.040	
b	0.35	0.50	0.014	0.020	
С	0.085	0.18	0.003	0.007	
D	2.80	3.04	0.110	0.120	
E	2.10	2.64	0.083	0.104	
E ₁	1.20	1.40	0.047	0.055	
е	0.95	BSC	0.037	4 Ref	
e ₁	1.90	BSC	0.0748 Ref		
L	0.40	0.60	0.016	0.024	
L ₁	0.64	0.64 Ref		S Ref	
S	0.50 Ref		0.020 Ref		
q	3°	8°	3°	8°	
ECN: S-03946-Rev. K. 09-	Jul-01				

DWG: 5479

Document Number: 71196 www.vishay.com 09-Jul-01



RECOMMENDED MINIMUM PADS FOR SOT-23



Recommended Minimum Pads Dimensions in Inches/(mm)

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APPLICATION NOTE



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