

V_{CES}	650V
$I_C(100^\circ\text{C})$	30A
$V_{CE(sat)} (Typ.)$	1.6V
P_D	194W

●Features

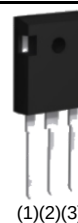
- 1) Low Collector - Emitter Saturation Voltage
- 2) High Speed Switching
- 3) Low Switching Loss & Soft Switching
- 4) Built in Very Fast & Soft Recovery FRD (RFN - Series)
- 5) Pb - free Lead Plating ; RoHS Compliant

●Applications

PFC
UPS
Power Conditioner
IH

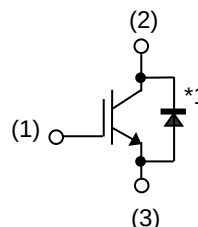
●Outline

TO-247GE



(1)(2)(3)

●Inner Circuit



(1) Gate
(2) Collector
(3) Emitter

*1 Built in FRD

●Packaging Specifications

Type	Packaging	Tube
	Reel Size (mm)	-
	Tape Width (mm)	-
	Basic Ordering Unit (pcs)	600
	Packing code	C13
	Marking	RGTH60TS65D

●Absolute Maximum Ratings (at $T_C = 25^\circ\text{C}$ unless otherwise specified)

Parameter		Symbol	Value	Unit
Collector - Emitter Voltage		V_{CES}	650	V
Gate - Emitter Voltage		V_{GES}	± 30	V
Collector Current	$T_C = 25^\circ\text{C}$	I_C	58	A
	$T_C = 100^\circ\text{C}$	I_C	30	A
Pulsed Collector Current		I_{CP}^{*1}	120	A
Diode Forward Current	$T_C = 25^\circ\text{C}$	I_F	40	A
	$T_C = 100^\circ\text{C}$	I_F	20	A
Diode Pulsed Forward Current		I_{FP}^{*1}	120	A
Power Dissipation	$T_C = 25^\circ\text{C}$	P_D	194	W
	$T_C = 100^\circ\text{C}$	P_D	97	W
Operating Junction Temperature		T_j	-40 to +175	$^\circ\text{C}$
Storage Temperature		T_{stg}	-55 to +175	$^\circ\text{C}$

*1 Pulse width limited by T_{jmax} .

● Thermal Resistance

Parameter	Symbol	Values			Unit
		Min.	Typ.	Max.	
Thermal Resistance IGBT Junction - Case	$R_{\theta(j-c)}$	-	-	0.77	°C/W
Thermal Resistance Diode Junction - Case	$R_{\theta(j-c)}$	-	-	2.00	°C/W

● IGBT Electrical Characteristics (at $T_j = 25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Conditions	Values			Unit
			Min.	Typ.	Max.	
Collector - Emitter Breakdown Voltage	BV_{CES}	$I_C = 10\mu\text{A}$, $V_{GE} = 0\text{V}$	650	-	-	V
Collector Cut - off Current	I_{CES}	$V_{CE} = 650\text{V}$, $V_{GE} = 0\text{V}$	-	-	10	μA
Gate - Emitter Leakage Current	I_{GES}	$V_{GE} = \pm 30\text{V}$, $V_{CE} = 0\text{V}$	-	-	± 200	nA
Gate - Emitter Threshold Voltage	$V_{GE(th)}$	$V_{CE} = 5\text{V}$, $I_C = 21.0\text{mA}$	4.5	5.5	6.5	V
Collector - Emitter Saturation Voltage	$V_{CE(sat)}$	$I_C = 30\text{A}$, $V_{GE} = 15\text{V}$ $T_j = 25^\circ\text{C}$	-	1.6	2.1	V
		$T_j = 175^\circ\text{C}$	-	2.1	-	

●IGBT Electrical Characteristics (at $T_j = 25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Conditions	Values			Unit
			Min.	Typ.	Max.	
Input Capacitance	C_{ies}	$V_{CE} = 30\text{V}$	-	1670	-	pF
Output Capacitance	C_{oes}	$V_{GE} = 0\text{V}$	-	66	-	
Reverse Transfer Capacitance	C_{res}	$f = 1\text{MHz}$	-	27	-	
Total Gate Charge	Q_g	$V_{CE} = 300\text{V}$	-	58	-	nC
Gate - Emitter Charge	Q_{ge}	$I_C = 30\text{A}$	-	15	-	
Gate - Collector Charge	Q_{gc}	$V_{GE} = 15\text{V}$	-	20	-	
Turn - on Delay Time	$t_{d(on)}$	$I_C = 30\text{A}, V_{CC} = 400\text{V}$	-	27	-	ns
Rise Time	t_r	$V_{GE} = 15\text{V}, R_G = 10\Omega$	-	40	-	
Turn - off Delay Time	$t_{d(off)}$	$T_j = 25^\circ\text{C}$	-	105	-	
Fall Time	t_f	Inductive Load	-	47	-	
Turn - on Delay Time	$t_{d(on)}$	$I_C = 30\text{A}, V_{CC} = 400\text{V}$	-	27	-	ns
Rise Time	t_r	$V_{GE} = 15\text{V}, R_G = 10\Omega$	-	40	-	
Turn - off Delay Time	$t_{d(off)}$	$T_j = 175^\circ\text{C}$	-	120	-	
Fall Time	t_f	Inductive Load	-	59	-	
Reverse Bias Safe Operating Area	RBSOA	$I_C = 120\text{A}, V_{CC} = 520\text{V}$ $V_P = 650\text{V}, V_{GE} = 15\text{V}$ $R_G = 60\Omega, T_j = 175^\circ\text{C}$	FULL SQUARE			-

●FRD Electrical Characteristics (at $T_j = 25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Conditions	Values			Unit
			Min.	Typ.	Max.	
Diode Forward Voltage	V_F	$I_F = 20\text{A}$ $T_j = 25^\circ\text{C}$ $T_j = 175^\circ\text{C}$	- -	1.35 1.15	1.8 -	V
Diode Reverse Recovery Time	t_{rr}	$I_F = 20\text{A}$ $V_{CC} = 400\text{V}$ $di_F/dt = 200\text{A}/\mu\text{s}$ $T_j = 25^\circ\text{C}$	-	58	-	ns
Diode Peak Reverse Recovery Current	I_{rr}		-	6.5	-	A
Diode Reverse Recovery Charge	Q_{rr}		-	0.21	-	μC
Diode Reverse Recovery Time	t_{rr}		-	236	-	ns
Diode Peak Reverse Recovery Current	I_{rr}	$V_{CC} = 400\text{V}$ $di_F/dt = 200\text{A}/\mu\text{s}$ $T_j = 175^\circ\text{C}$	-	10.7	-	A
Diode Reverse Recovery Charge	Q_{rr}		-	1.36	-	μC

●Electrical Characteristic Curves

Fig.1 Power Dissipation vs. Case Temperature

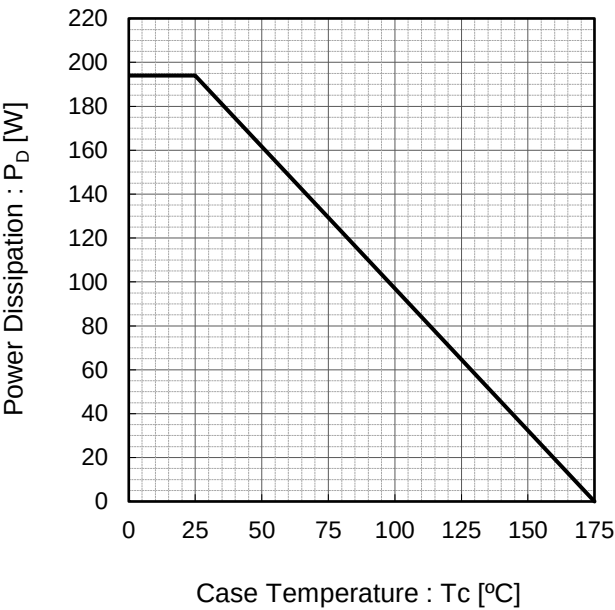


Fig.2 Collector Current vs. Case Temperature

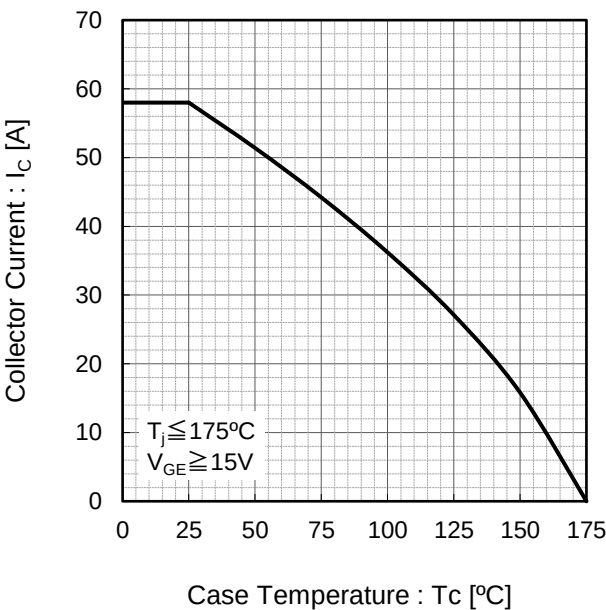


Fig.3 Forward Bias Safe Operating Area

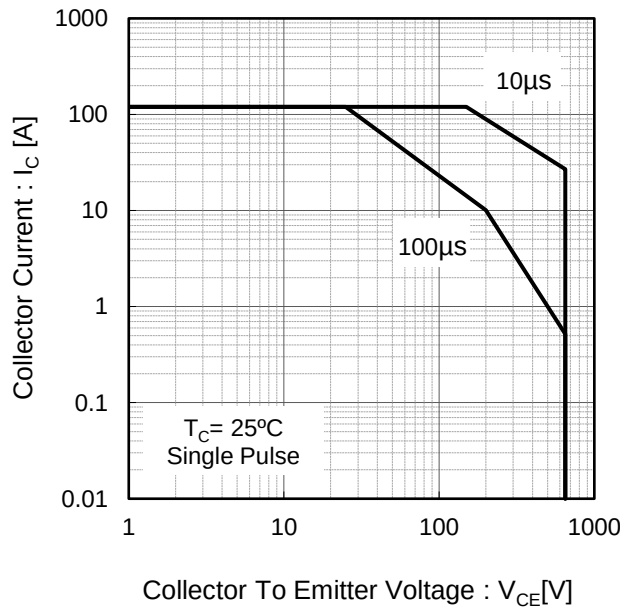
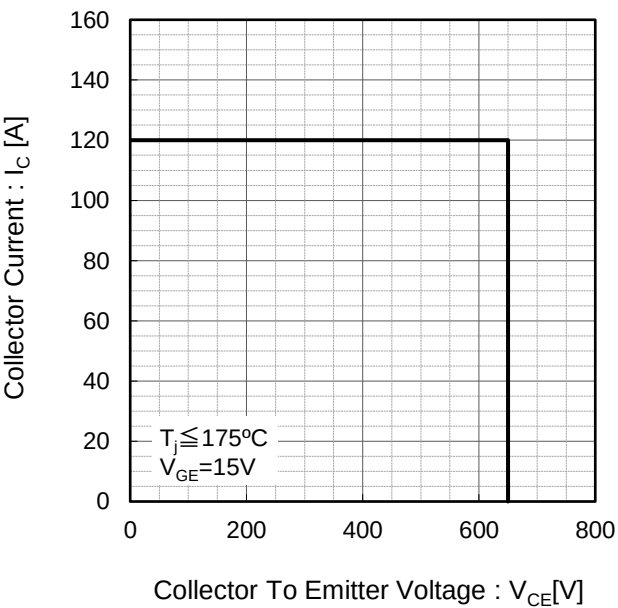


Fig.4 Reverse Bias Safe Operating Area



●Electrical Characteristic Curves

Fig.5 Typical Output Characteristics

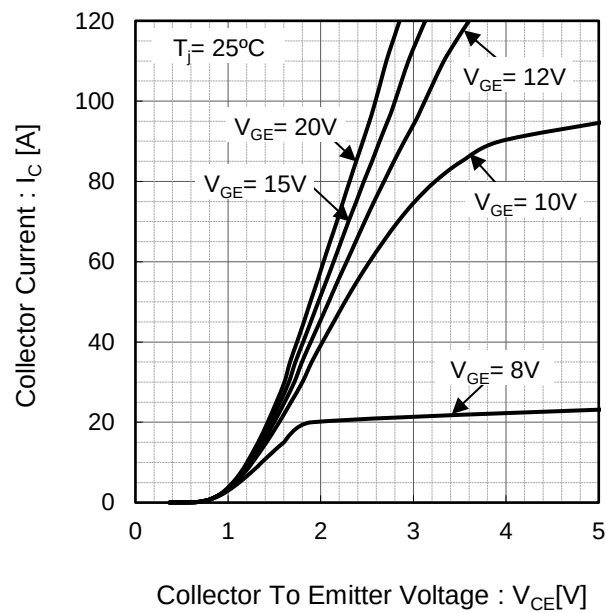


Fig.6 Typical Output Characteristics

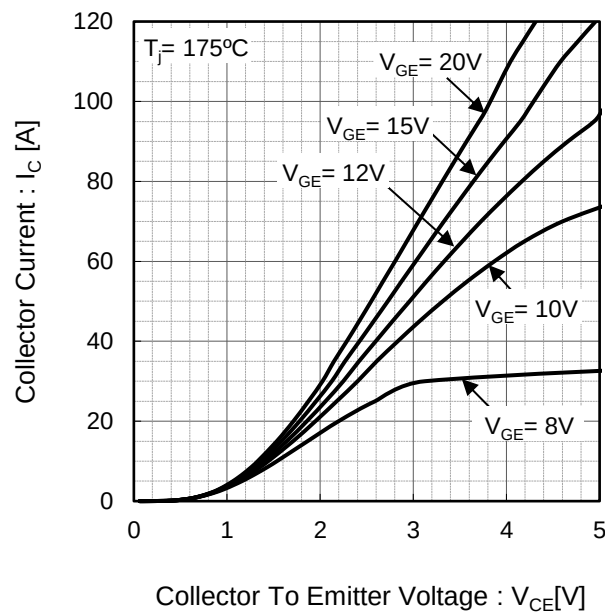


Fig.7 Typical Transfer Characteristics

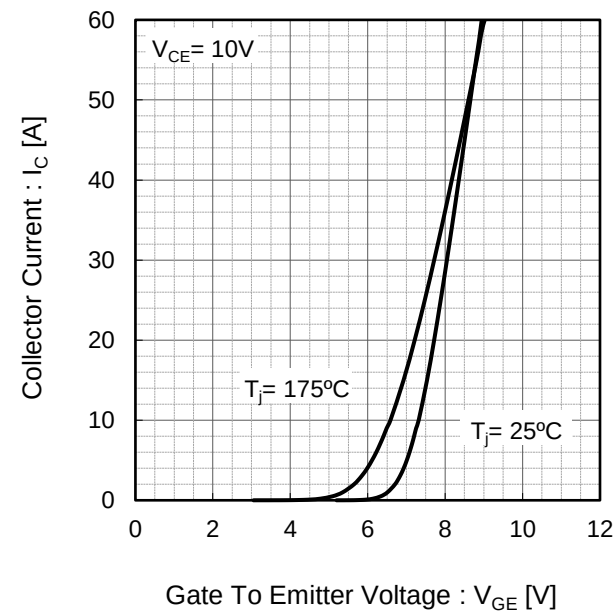
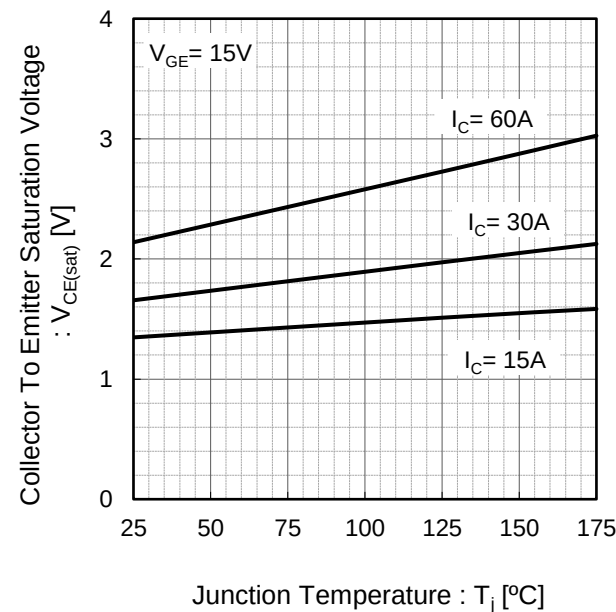


Fig.8 Typical Collector To Emitter Saturation Voltage vs. Junction Temperature



●Electrical Characteristic Curves

Fig.9 Typical Collector To Emitter Saturation Voltage vs. Gate To Emitter Voltage

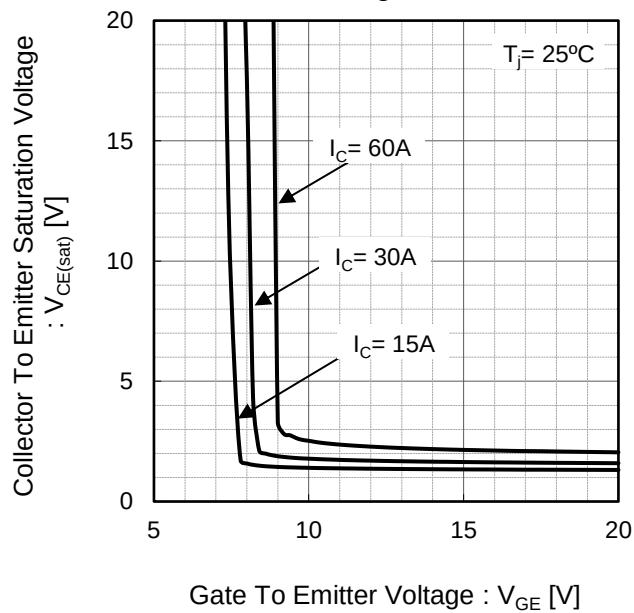


Fig.10 Typical Collector To Emitter Saturation Voltage vs. Gate To Emitter Voltage

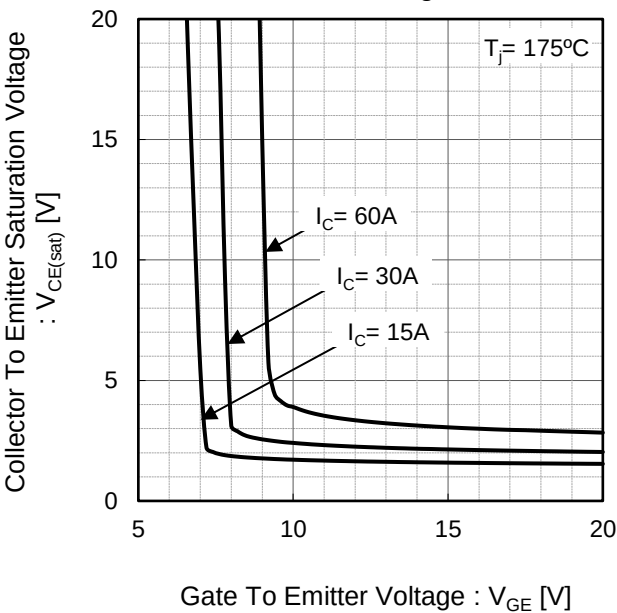


Fig.11 Typical Switching Time vs. Collector Current

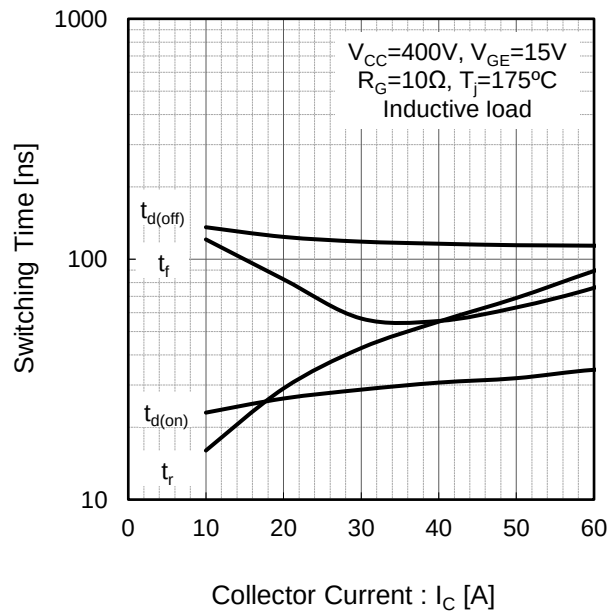
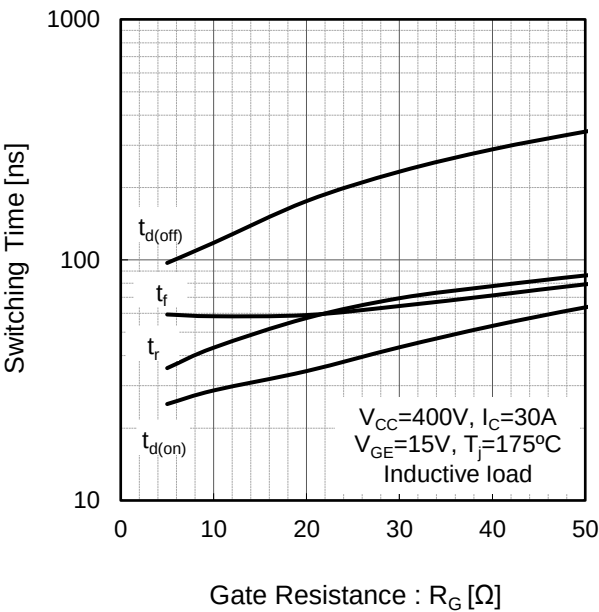


Fig.12 Typical Switching Time vs. Gate Resistance



●Electrical Characteristic Curves

Fig.13 Typical Switching Energy Losses vs. Collector Current

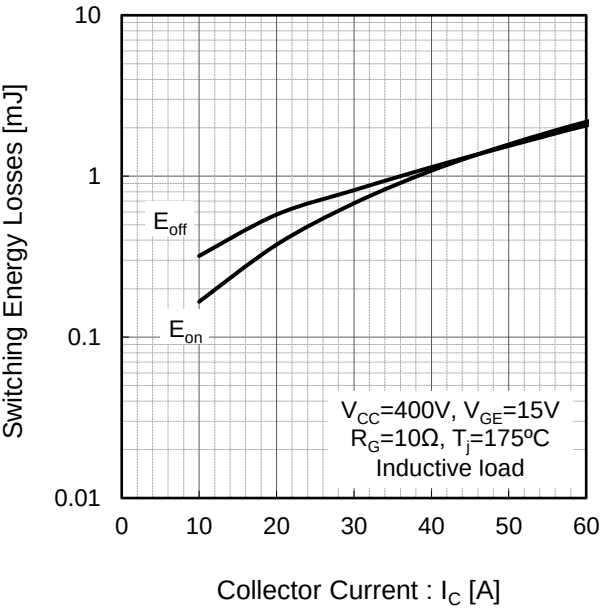


Fig.14 Typical Switching Energy Losses vs. Gate Resistance

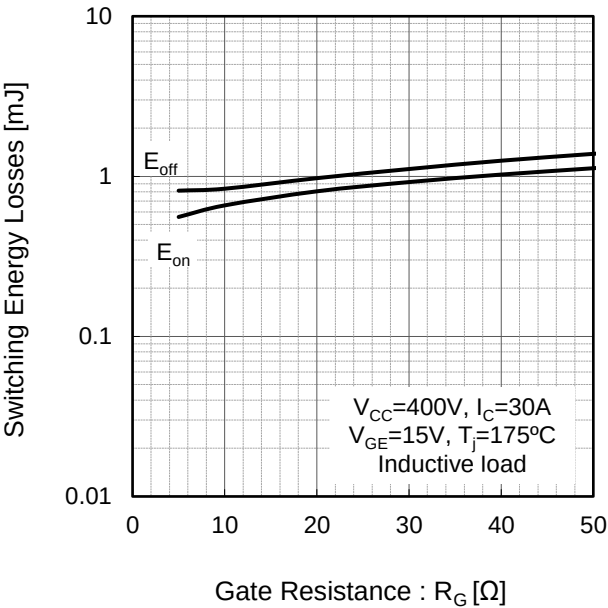


Fig.15 Typical Capacitance vs. Collector To Emitter Voltage

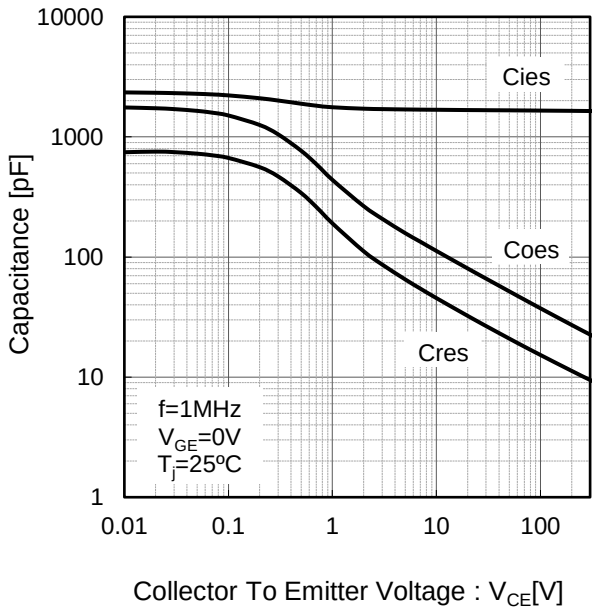
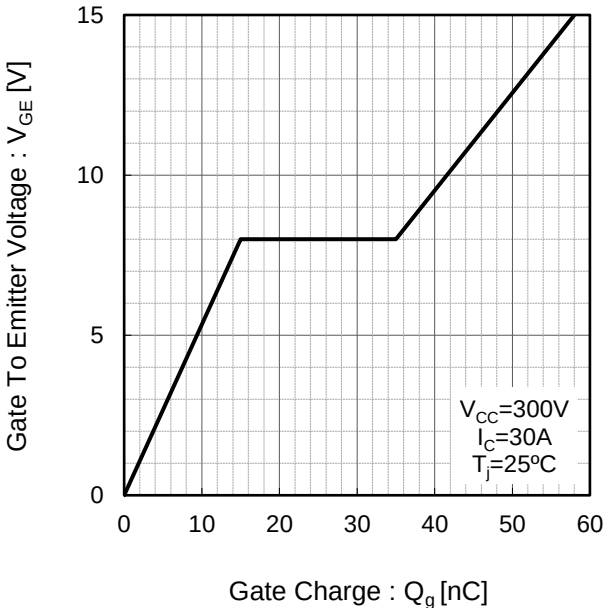


Fig.16 Typical Gate Charge



●Electrical Characteristic Curves

Fig.17 Typical Diode Forward Current vs. Forward Voltage

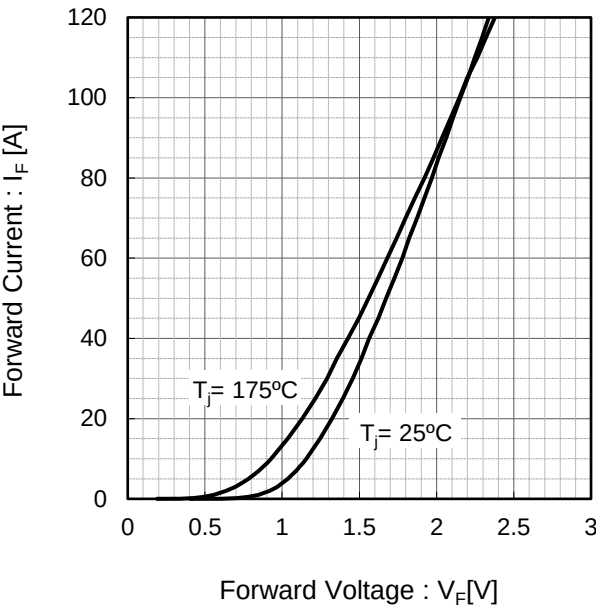


Fig.18 Typical Diode Reverse Recovery Time vs. Forward Current

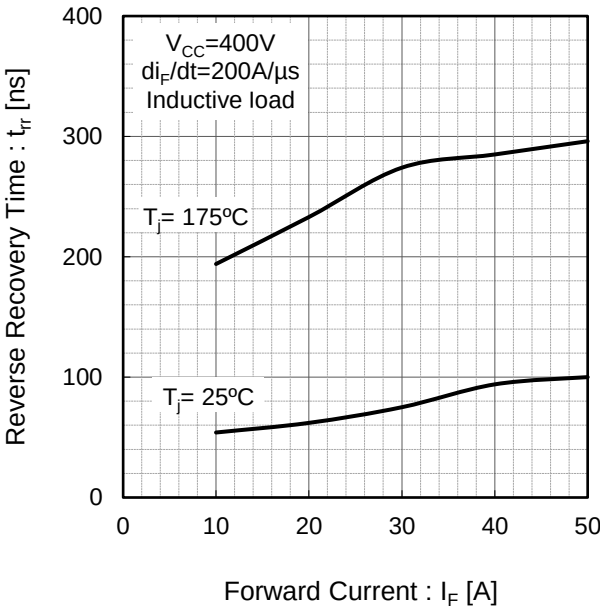


Fig.19 Typical Diode Reverse Recovery Current vs. Forward Current

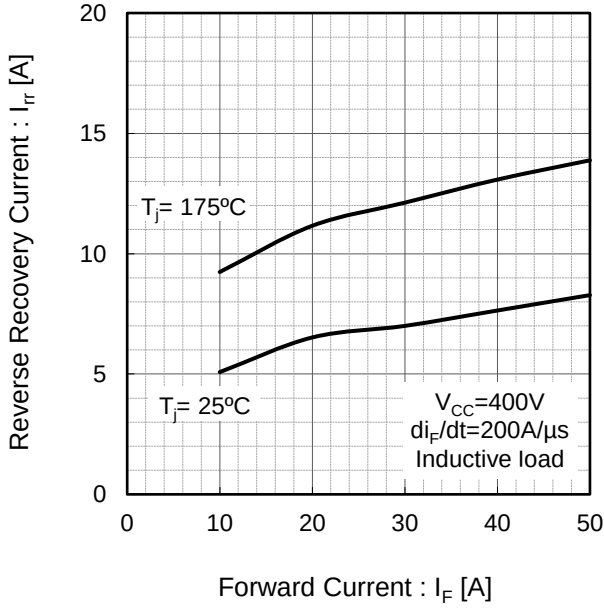
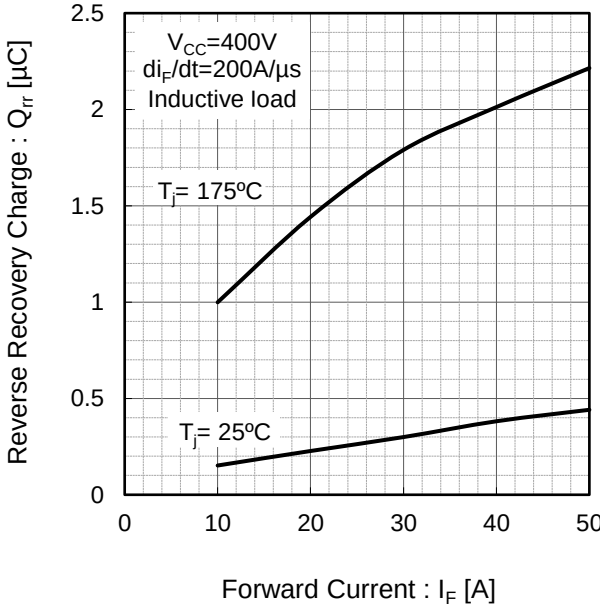


Fig.20 Typical Diode Reverse Recovery Charge vs. Forward Current



●Electrical Characteristic Curves

Fig.21 IGBT Transient Thermal Impedance

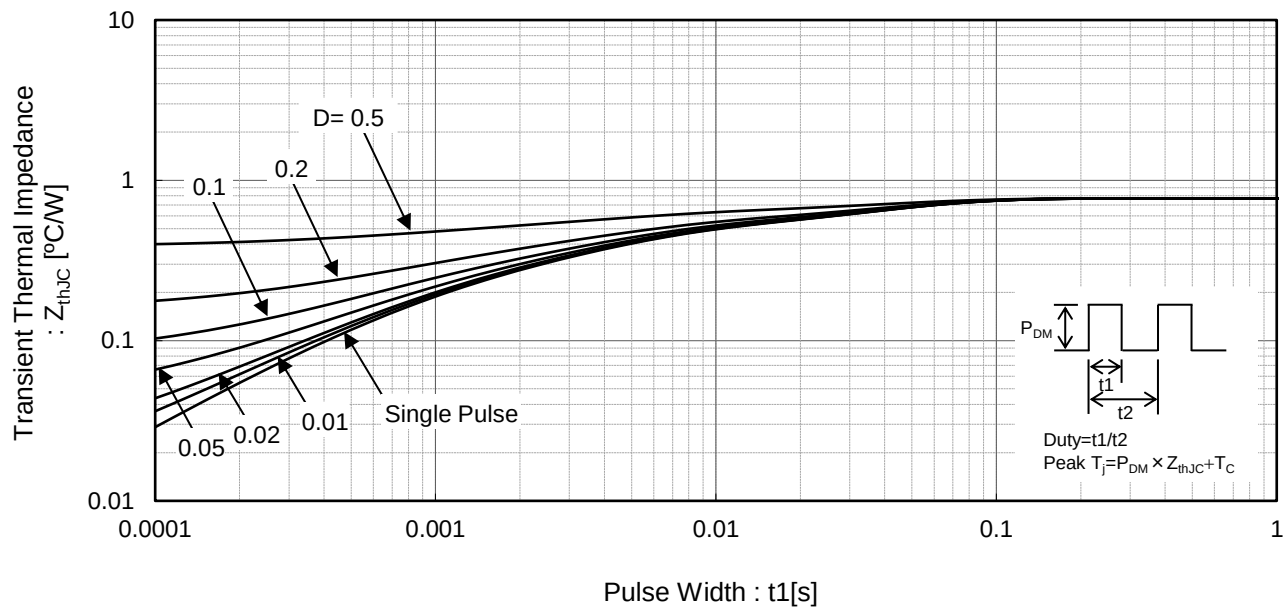
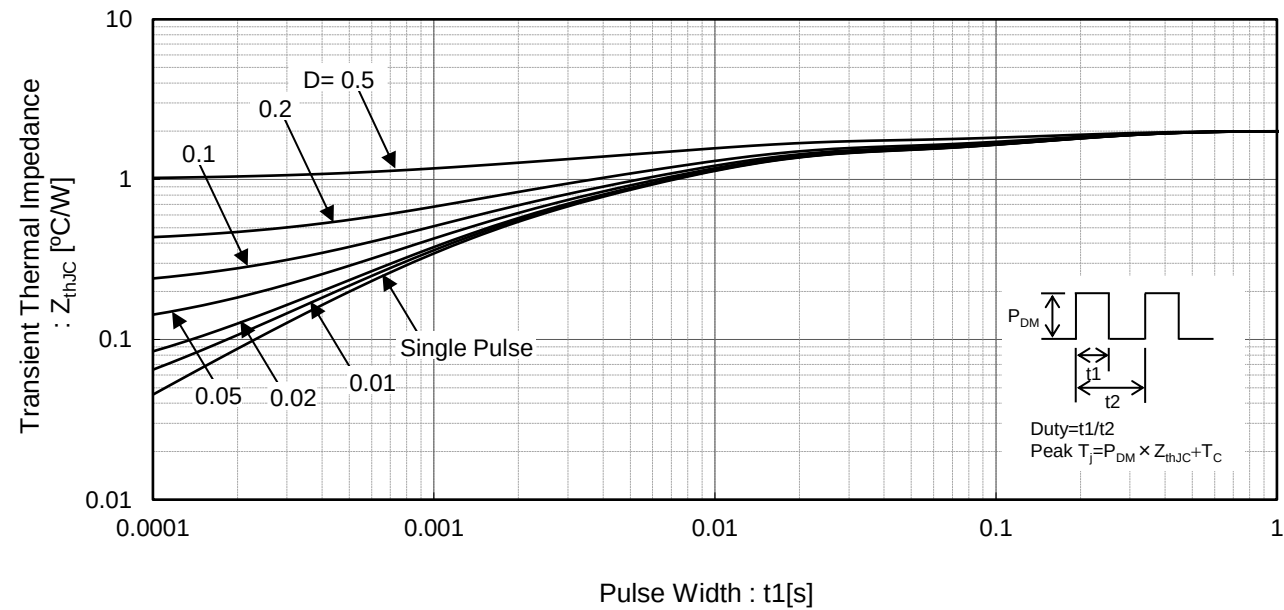


Fig.22 Diode Transient Thermal Impedance



●Inductive Load Switching Circuit and Waveform

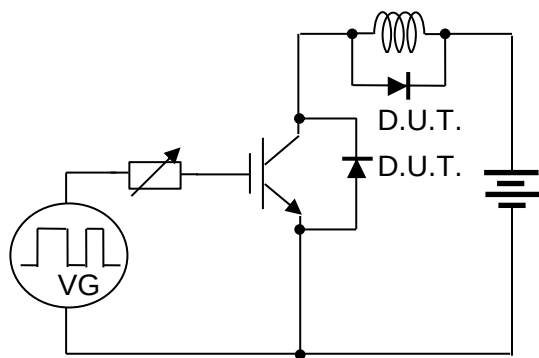


Fig.23 Inductive Load Circuit

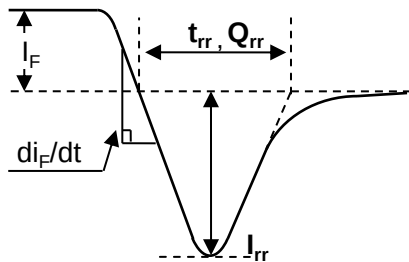


Fig.24 Diode Reverse Recovery Waveform

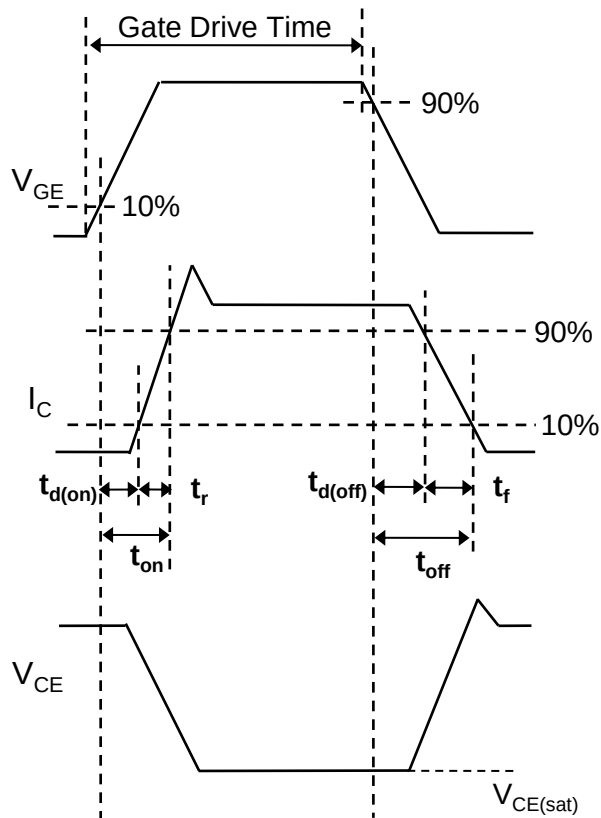


Fig.25 Inductive Load Waveform