

Product Summary

BV _{DSS}	R _{DS(ON)} Max	I _D Max T _A = +25°C
-30V	5Ω @ V _{GS} = -4.5V	-0.2A
	6Ω @ V _{GS} = -2.5V	-0.18A
	7Ω @ V _{GS} = -1.8V	-0.17A
	10Ω @ V _{GS} = -1.5V	-0.14A

Features and Benefits

- Low Package Profile
- 0.6mm × 0.4mm Package Footprint
- Low On-Resistance
- Very Low Gate Threshold Voltage, -1.0V Max
- ESD Protected Gate
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen- and Antimony-Free. "Green" Device (Note 3)**
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please [contact us](https://www.diodes.com/quality/product-definitions/) or your local Diodes representative.

<https://www.diodes.com/quality/product-definitions/>

Description and Applications

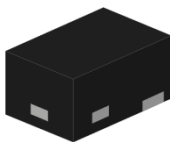
This MOSFET is designed to minimize the on-state resistance (R_{DS(ON)}) and yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

- General Purpose Interfacing Switch
- Power Management Functions
- Analog Switch

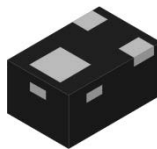
Mechanical Data

- Case: X2-DFN0604-3
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish—NiPdAu over Copper Lead-Frame; Solderable per MIL-STD-202, Method 208 [e4](#)
- Weight: 0.001 grams (Approximate)

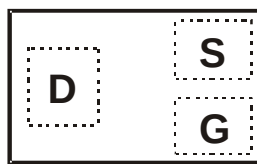
X2-DFN0604-3



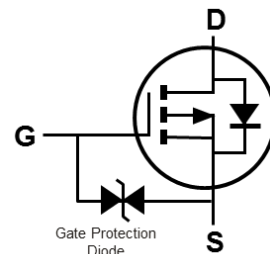
Top View



Bottom View



Top View
Package Pin Configuration



Equivalent Circuit

Ordering Information (Note 4)

Part Number	Case	Packaging
DMP32D9UFO-7B	X2-DFN0604-3	10k /Tape & Reel

- Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
 2. See <https://www.diodes.com/quality/lead-free/> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
 4. For packaging details, go to our website at <https://www.diodes.com/design/support/packaging/diodes-packaging/>.

Marking Information



Top View

BE = Product Type Marking Code
Bar Denotes Gate and Source Side

Maximum Ratings (@ $T_A = +25^\circ\text{C}$, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			V_{DSS}	-30	V
Gate-Source Voltage			V_{GSS}	± 12	V
Continuous Drain Current (Note 5) $V_{GS} = -4.5V$	Steady State	$T_A = +25^{\circ}C$	I_D	-0.2	A
		$T_A = +70^{\circ}C$		-0.16	
Maximum Continuous Body Diode Forward Current (Note 6)			I_S	-0.8	A
Pulsed Drain Current (Note 6)			I_{DM}	-0.8	A

Thermal Characteristics (@ $T_A = +25^\circ\text{C}$, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)	Steady State	P_D	320	mW
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	$R_{\theta JA}$	386	$^\circ\text{C/W}$
Operating and Storage Temperature Range		T_J, T_{STG}	-55 to +150	$^\circ\text{C}$

Electrical Characteristics (@ $T_A = +25^\circ\text{C}$, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BV_{DSS}	-30	—	—	V	$V_{GS} = 0\text{V}, I_D = -250\mu\text{A}$
Zero Gate Voltage Drain Current	I_{DSS}	—	—	-100	nA	$V_{DS} = -24\text{V}, V_{GS} = 0\text{V}$
Gate-Source Leakage	I_{GSS}	—	—	± 10	μA	$V_{GS} = \pm 10\text{V}, V_{DS} = 0\text{V}$
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	$V_{GS(TH)}$	-0.4	—	-1.0	V	$V_{DS} = V_{GS}, I_D = -250\mu\text{A}$
Static Drain-Source On-Resistance	$R_{DS(ON)}$	—	1.9	5	Ω	$V_{GS} = -4.5\text{V}, I_D = -100\text{mA}$
		—	2.5	6		$V_{GS} = -2.5\text{V}, I_D = -50\text{mA}$
		—	3.2	7		$V_{GS} = -1.8\text{V}, I_D = -20\text{mA}$
		—	3.7	10		$V_{GS} = -1.5\text{V}, I_D = -10\text{mA}$
		—	—	—		$V_{GS} = -1.5\text{V}, I_D = -10\text{mA}$
Diode Forward Voltage	V_{SD}	—	-0.6	-1.0	V	$V_{GS} = 0\text{V}, I_S = -10\text{mA}$
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	C_{iss}	—	21.8	—	pF	$V_{DS} = -15\text{V}, V_{GS} = 0\text{V}, f = 1.0\text{MHz}$
Output Capacitance	C_{oss}	—	2.82	—	pF	
Reverse Transfer Capacitance	C_{rss}	—	1.66	—	pF	
Total Gate Charge	Q_g	—	0.35	—	nC	$V_{GS} = -4.5\text{V}, V_{DS} = -15\text{V}, I_D = -200\text{mA}$
Gate-Source Charge	Q_{gs}	—	0.05	—	nC	
Gate-Drain Charge	Q_{gd}	—	0.10	—	nC	
Turn-On Delay Time	$t_{D(ON)}$	—	3.5	—	ns	$V_{DD} = -15\text{V}, V_{GS} = -4.5\text{V}, R_g = 2\Omega, I_D = -200\text{mA}$
Turn-On Rise Time	t_R	—	5.2	—	ns	
Turn-Off Delay Time	$t_{D(OFF)}$	—	18.8	—	ns	
Turn-Off Fall Time	t_F	—	8.7	—	ns	

- Notes:
- Device mounted on FR-4 PCB, with minimum recommended pad layout.
 - Device mounted on minimum recommended pad layout test board, 10 μs pulse duty cycle = 1%.
 - Short duration pulse test used to minimize self-heating effect.
 - Guaranteed by design. Not subject to product testing.

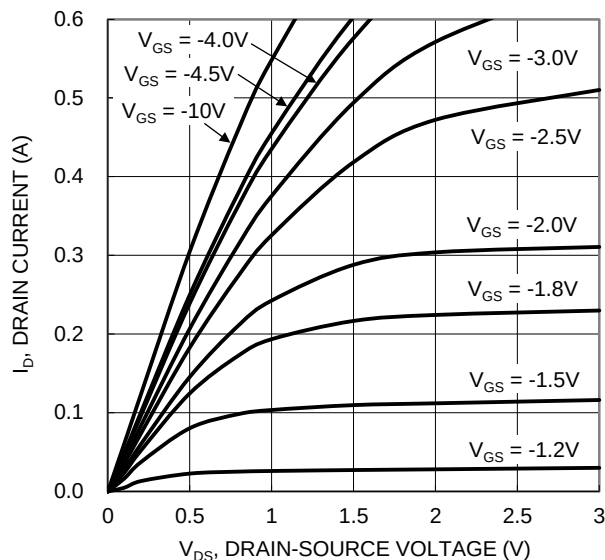


Figure 1. Typical Output Characteristic

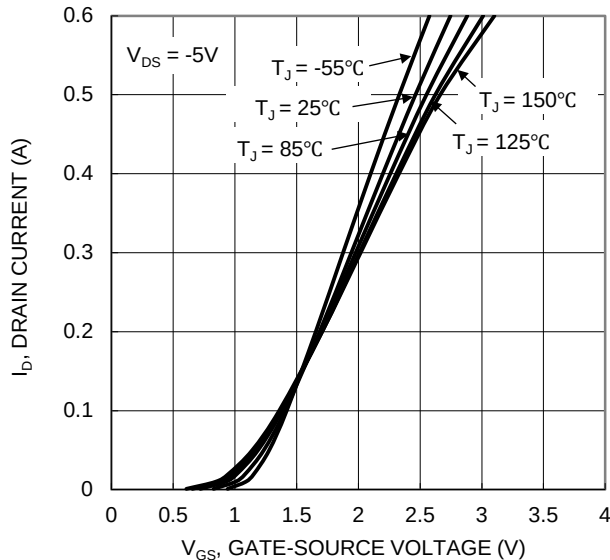


Figure 2. Typical Transfer Characteristic

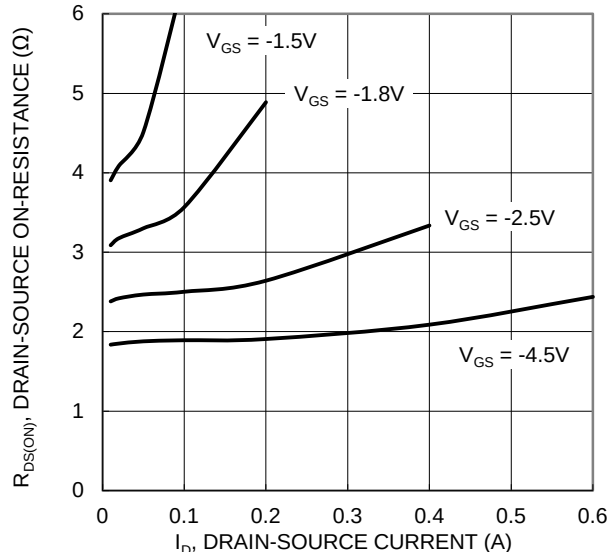


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

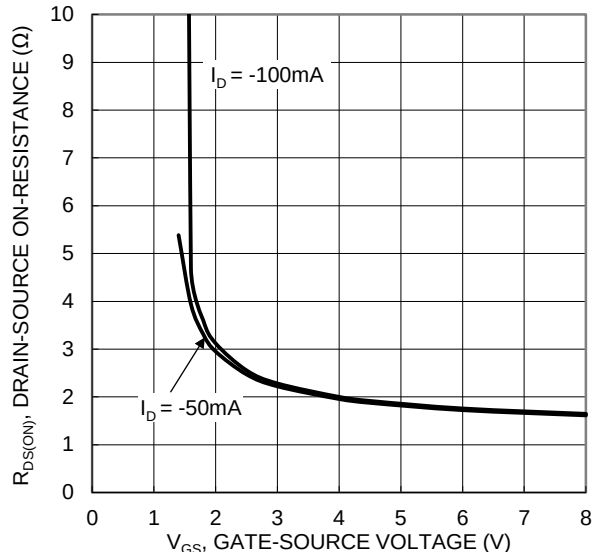


Figure 4. Typical Transfer Characteristic

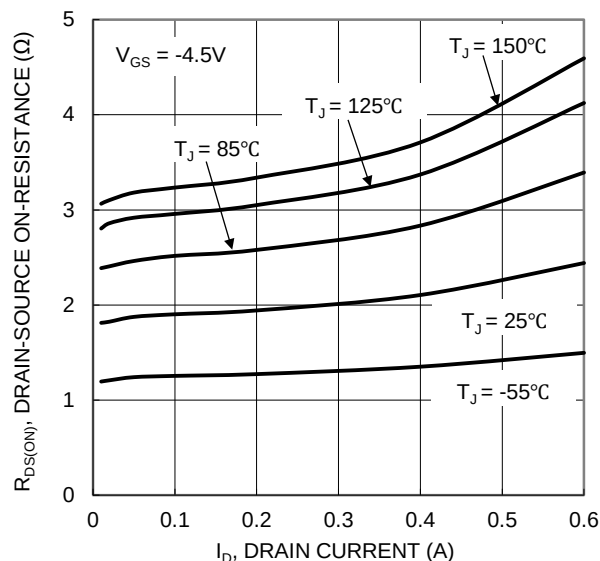


Figure 5. Typical On-Resistance vs. Drain Current and Junction Temperature

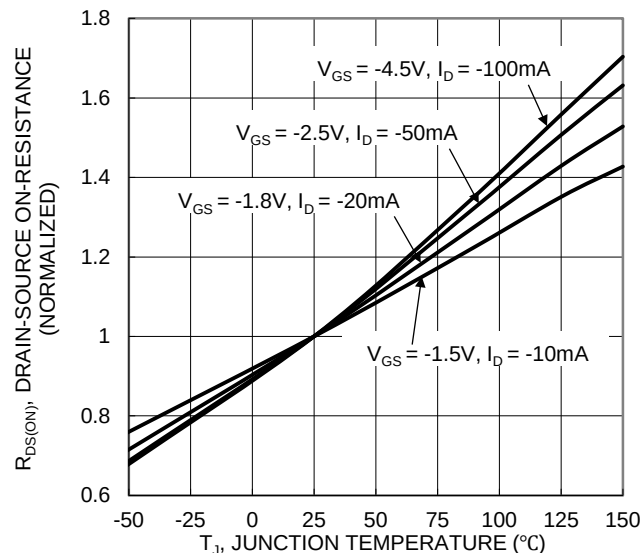


Figure 6. On-Resistance Variation with Junction Temperature

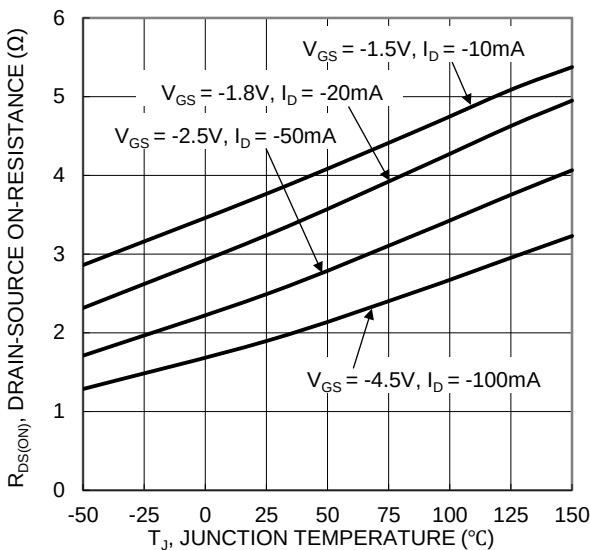


Figure 7. On-Resistance Variation with Junction Temperature

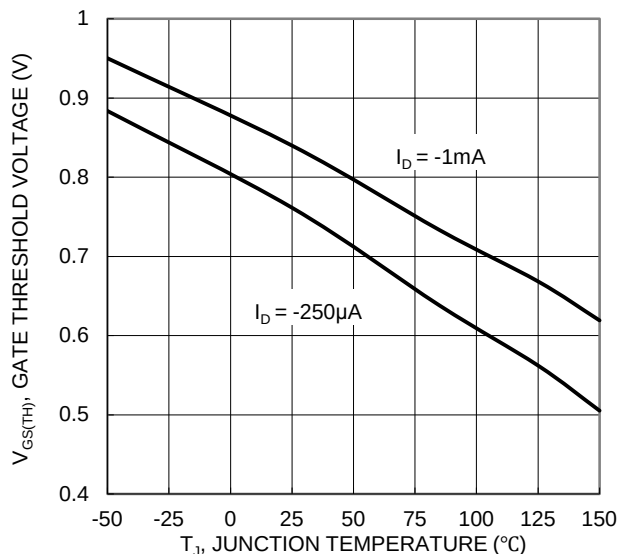


Figure 8. Gate Threshold Variation vs. Junction Temperature

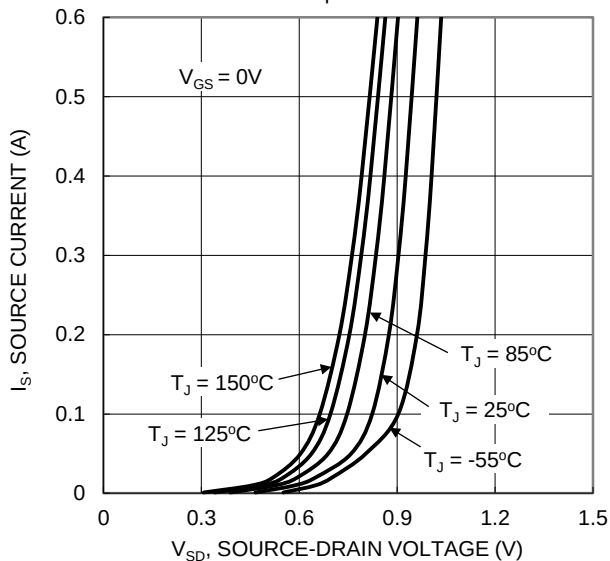


Figure 9. Diode Forward Voltage vs. Current

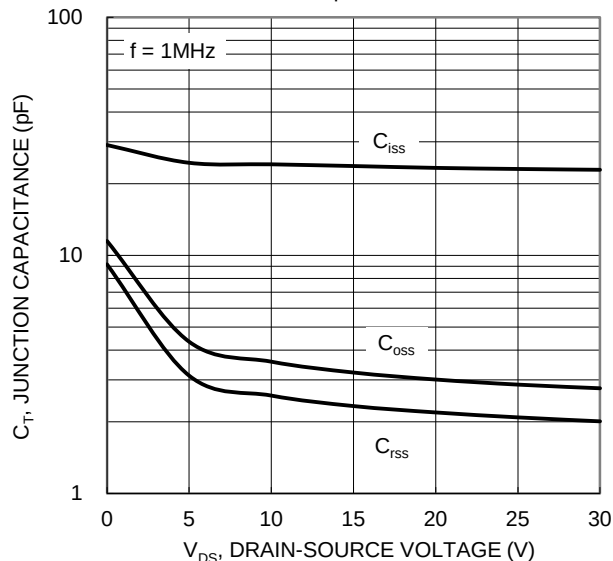


Figure 10. Typical Junction Capacitance

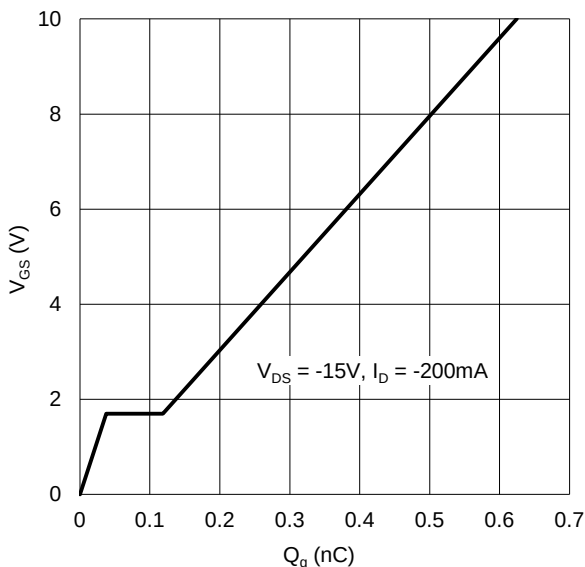


Figure 11. Gate Charge

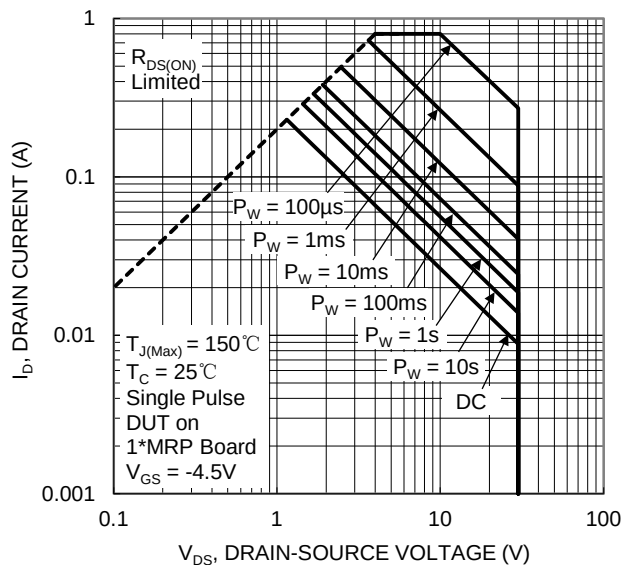


Figure 12. SOA, Safe Operation Area

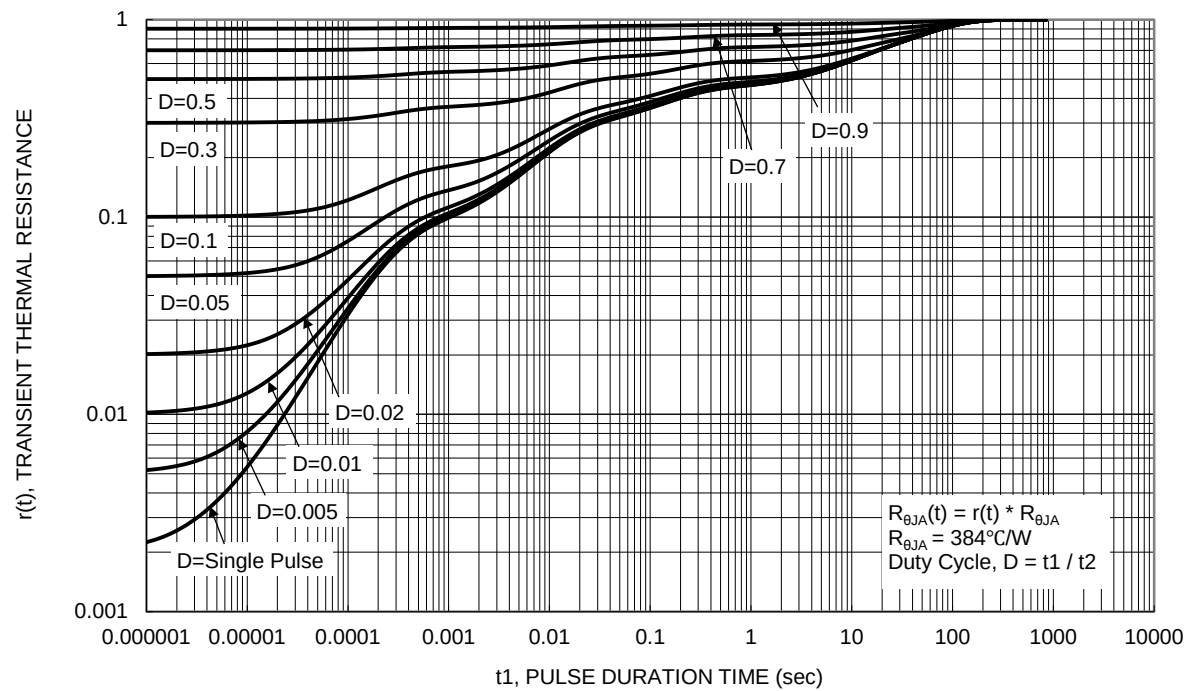
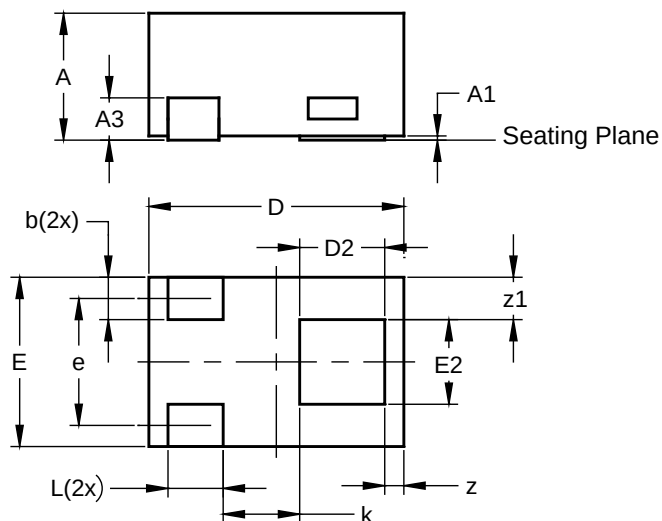


Figure 13. Transient Thermal Resistance

Package Outline Dimensions

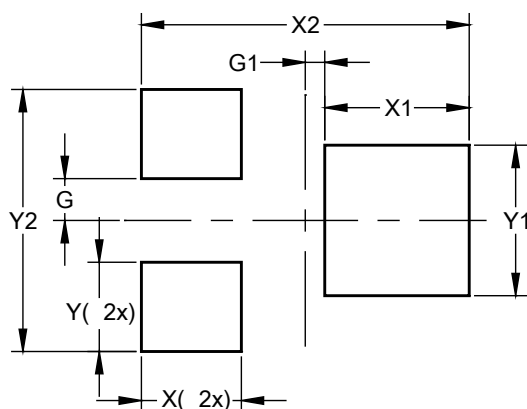
Please see <http://www.diodes.com/package-outlines.html> for the latest version.



X2-DFN0604-3			
Dim	Min	Max	Typ
A	--	0.40	0.36
A1	0.00	0.03	0.02
A3	--	--	0.10
b	0.07	0.15	0.10
D	0.55	0.65	0.60
D2	0.15	0.25	0.20
E	0.35	0.45	0.40
E2	0.15	0.25	0.20
e	--	--	0.30
k	0.15	--	--
L	0.10	0.18	0.13
z	--	--	0.045
z1	--	--	0.10
All Dimensions in mm			

Suggested Pad Layout

Please see <http://www.diodes.com/package-outlines.html> for the latest version.



Dimensions	Value (in mm)
G	0.075
G1	0.035
X	0.180
X1	0.260
X2	0.590
Y	0.160
Y1	0.270
Y2	0.470

IMPORTANT NOTICE

DIODES INCORPORATED MAKES NO WARRANTY OF ANY KIND, EXPRESS OR IMPLIED, WITH REGARDS TO THIS DOCUMENT, INCLUDING, BUT NOT LIMITED TO, THE IMPLIED WARRANTIES OF MERCHANTABILITY AND FITNESS FOR A PARTICULAR PURPOSE (AND THEIR EQUIVALENTS UNDER THE LAWS OF ANY JURISDICTION).

Diodes Incorporated and its subsidiaries reserve the right to make modifications, enhancements, improvements, corrections or other changes without further notice to this document and any product described herein. Diodes Incorporated does not assume any liability arising out of the application or use of this document or any product described herein; neither does Diodes Incorporated convey any license under its patent or trademark rights, nor the rights of others. Any Customer or user of this document or products described herein in such applications shall assume all risks of such use and will agree to hold Diodes Incorporated and all the companies whose products are represented on Diodes Incorporated website, harmless against all damages.

Diodes Incorporated does not warrant or accept any liability whatsoever in respect of any products purchased through unauthorized sales channel. Should Customers purchase or use Diodes Incorporated products for any unintended or unauthorized application, Customers shall indemnify and hold Diodes Incorporated and its representatives harmless against all claims, damages, expenses, and attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized application.

Products described herein may be covered by one or more United States, international or foreign patents pending. Product names and markings noted herein may also be covered by one or more United States, international or foreign trademarks.

This document is written in English but may be translated into multiple languages for reference. Only the English version of this document is the final and determinative format released by Diodes Incorporated.

LIFE SUPPORT

Diodes Incorporated products are specifically not authorized for use as critical components in life support devices or systems without the express written approval of the Chief Executive Officer of Diodes Incorporated. As used herein:

A. Life support devices or systems are devices or systems which:

1. are intended to implant into the body, or
2. support or sustain life and whose failure to perform when properly used in accordance with instructions for use provided in the labeling can be reasonably expected to result in significant injury to the user.

B. A critical component is any component in a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or to affect its safety or effectiveness.

Customers represent that they have all necessary expertise in the safety and regulatory ramifications of their life support devices or systems, and acknowledge and agree that they are solely responsible for all legal, regulatory and safety-related requirements concerning their products and any use of Diodes Incorporated products in such safety-critical, life support devices or systems, notwithstanding any devices- or systems-related information or support that may be provided by Diodes Incorporated. Further, Customers must fully indemnify Diodes Incorporated and its representatives against any damages arising out of the use of Diodes Incorporated products in such safety-critical, life support devices or systems.

Copyright © 2020, Diodes Incorporated

www.diodes.com