

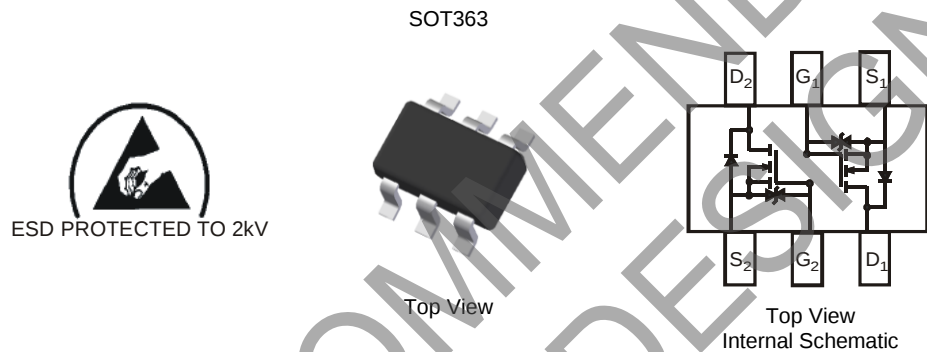
DUAL N-CHANNEL ENHANCEMENT MODE MOSFET

Features

- Dual N-Channel MOSFET
- Low On-Resistance (1.0V Max)
- Very Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- Ultra-Small Surface Mount Package
- **ESD Protected up to 2kV**
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- **Qualified to AEC-Q101 Standards for High Reliability**

Mechanical Data

- Case: SOT363
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram
- Terminals: Finish – Matte Tin Annealed over Alloy 42 Leadframe. Solderable per MIL-STD-202, Method 208^③
- Weight: 0.006 grams (Approximate)



Ordering Information (Note 4)

Part Number	Case	Packaging
DMN5L06DWK-7	SOT363	3,000/Tape & Reel

- Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
 2. See <https://www.diodes.com/quality/lead-free/> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
 4. For packaging details, go to our website at <https://www.diodes.com/design/support/packaging/diodes-packaging/>.

Marking Information



DAB = Marking Code
YM = Date Code Marking
Y = Year ex: G = 2019
M = Month ex: 9 = September

Date Code Key

Year	2006	2007	2008	...	2012	2013	2014	2015	2016	2017	2018	2019
Code	T	U	V	...	Z	A	B	C	D	E	F	G

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Drain Source Voltage	V _{DSS}	50	V
Gate-Source Voltage	V _{GSS}	±20	V
Drain Current	I _D	305 800	mA
		Continuous Pulsed (Note 6)	

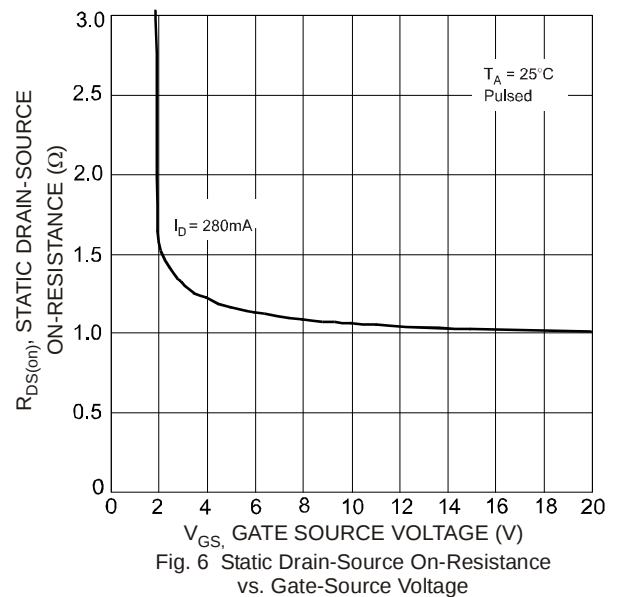
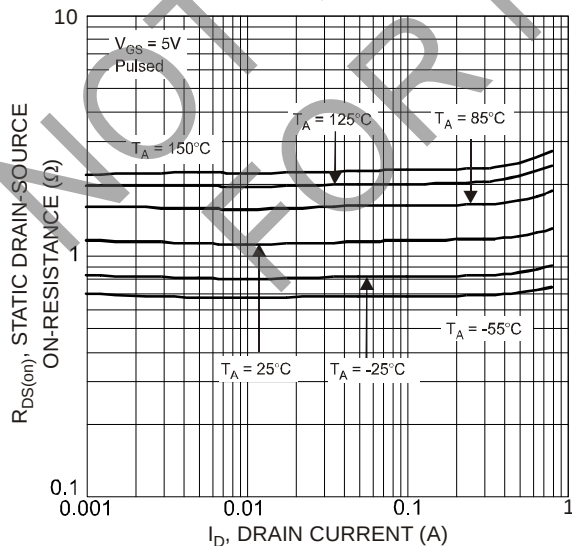
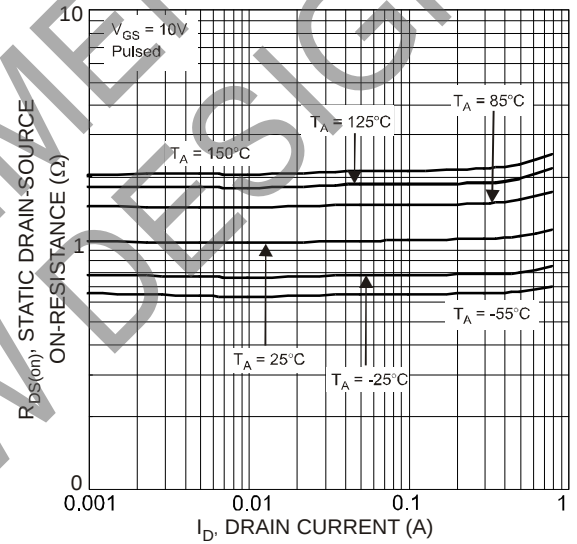
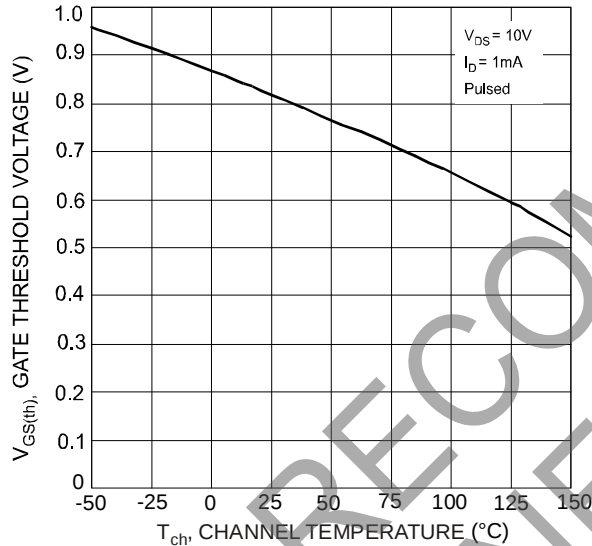
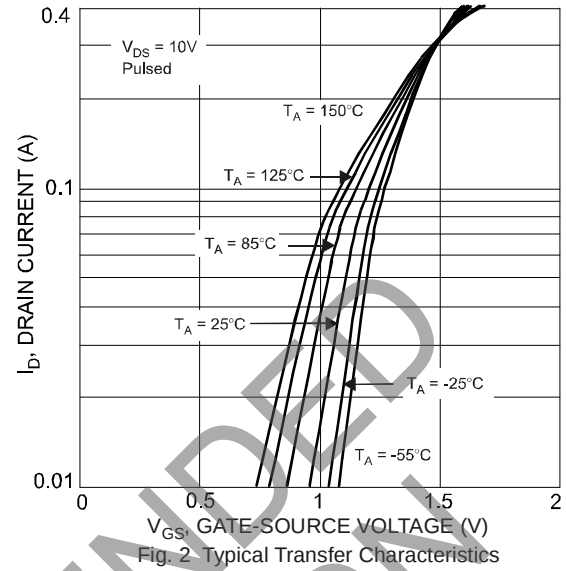
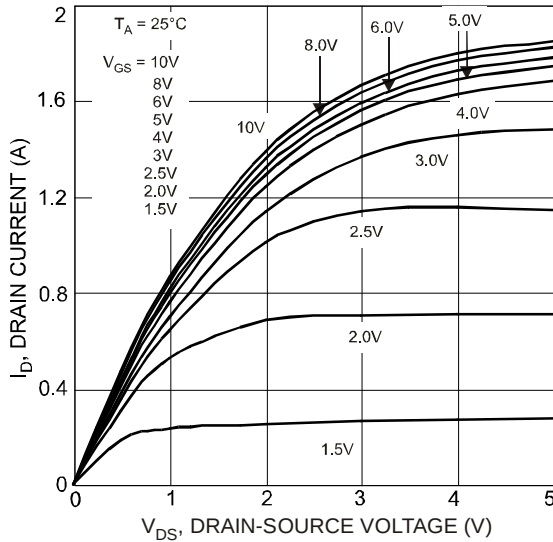
Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 5)	P _D	250	mW
Thermal Resistance, Junction to Ambient	R _{θJA}	500	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-65 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BV _{DSS}	50	—	—	V	V _{GS} = 0V, I _D = 10μA
Zero Gate Voltage Drain Current @ T _C = +25°C	I _{DSS}	—	—	60	nA	V _{DS} = 50V, V _{GS} = 0V
Gate-Body Leakage	I _{GSS}	—	—	1 500 50	μA nA nA	V _{GS} = ±12V, V _{DS} = 0V V _{GS} = ±10V, V _{DS} = 0V V _{GS} = ±5V, V _{DS} = 0V
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	V _{GS(TH)}	0.49	—	1.0	V	V _{DS} = V _{GS} , I _D = 250μA
Static Drain-Source On-Resistance	R _{DS(ON)}	—	—	3.0 2.5 2.0	Ω	V _{GS} = 1.8V, I _D = 50mA V _{GS} = 2.5V, I _D = 50mA V _{GS} = 5.0V, I _D = 50mA
On-State Drain Current	I _{D(ON)}	0.5	1.4	—	A	V _{GS} = 10V, V _{DS} = 7.5V
Forward Transconductance	Y _{FS}	200	—	—	mS	V _{DS} = 10V, I _D = 0.2A
Source-Drain Diode Forward Voltage	V _{SD}	0.5	—	1.4	V	V _{GS} = 0V, I _S = 115mA
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	C _{iss}	—	—	50	pF	V _{DS} = 25V, V _{GS} = 0V f = 1.0MHz
Output Capacitance	C _{oss}	—	—	25	pF	
Reverse Transfer Capacitance	C _{rss}	—	—	5.0	pF	
Gate Resistance	R _G	—	65	—	Ω	V _{DS} = 0V, V _{GS} = 0V, f = 1MHz
Total Gate Charge	Q _G	—	0.4	—	nC	V _{GS} = 4.5V, V _{DS} = 10V, I _D = 0.25A
Gate-Source Charge	Q _{GS}	—	0.1	—	nC	
Gate-Drain Charge	Q _{GD}	—	0.1	—	nC	
Turn-On Delay Time	t _{D(ON)}	—	2.1	—	ns	V _{DD} = 30V, V _{GS} = 10V, R _G = 25Ω, I _D = 0.2A
Turn-On Rise Time	t _R	—	1.8	—	ns	
Turn-Off Delay Time	t _{D(OFF)}	—	14.4	—	ns	
Turn-Off Fall Time	t _F	—	8.4	—	ns	

- Notes:
5. Device mounted on FR-4 PCB.
 6. Pulse width ≤10μs, Duty Cycle ≤1%.
 7. Short duration pulse test used to minimize self-heating effect.
 8. Guaranteed by design. Not subject to product testing.



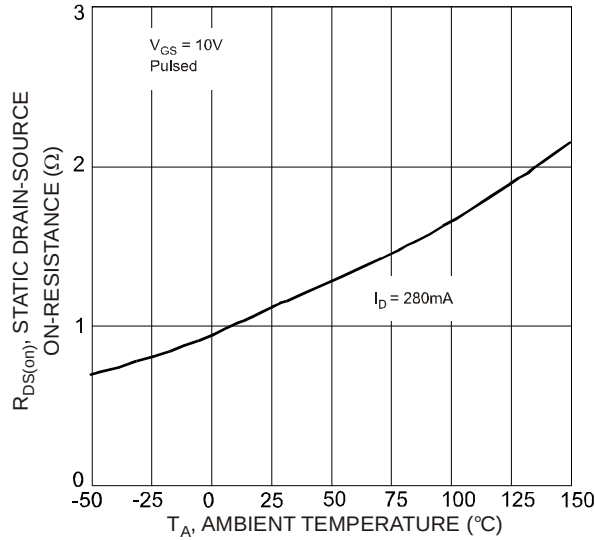


Fig. 7 Static Drain-Source On-State Resistance vs. Ambient Temperature

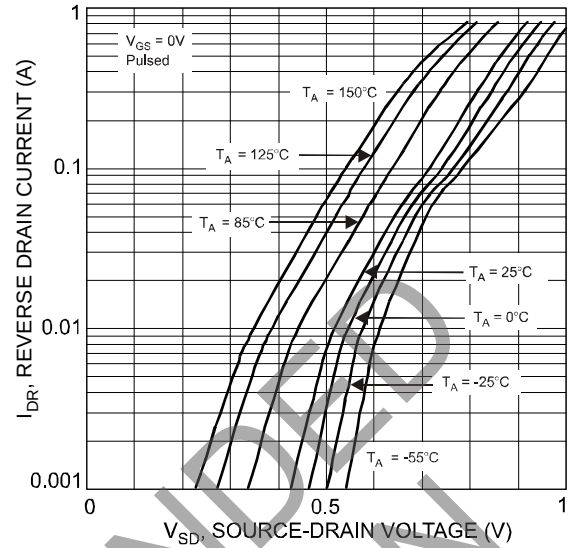


Fig. 8 Reverse Drain Current vs. Source-Drain Voltage

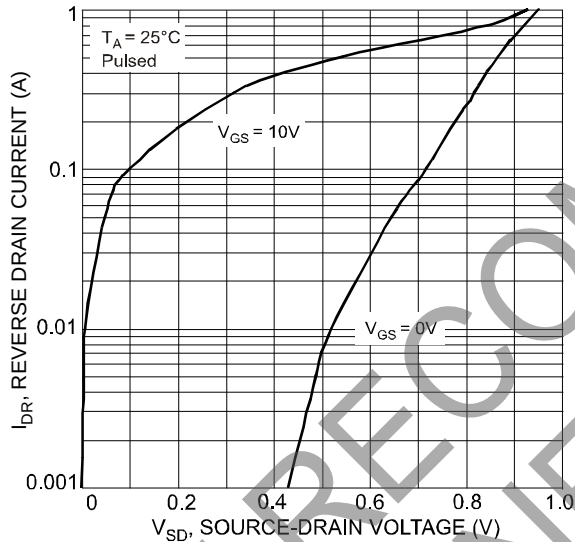


Fig. 9 Reverse Drain Current vs. Source-Drain Voltage

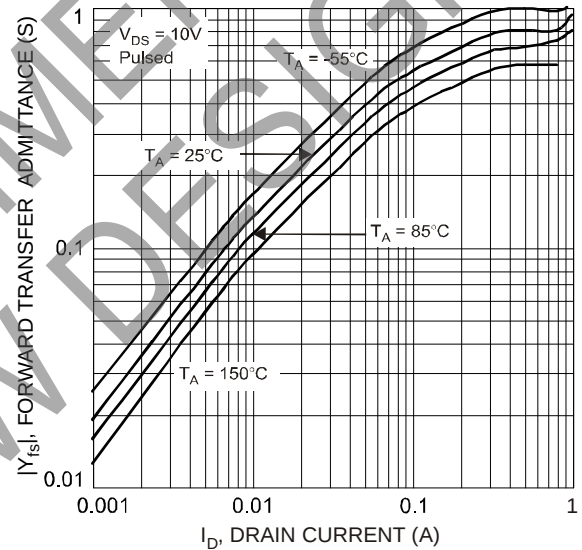


Fig. 10 Forward Transfer Admittance vs. Drain Current

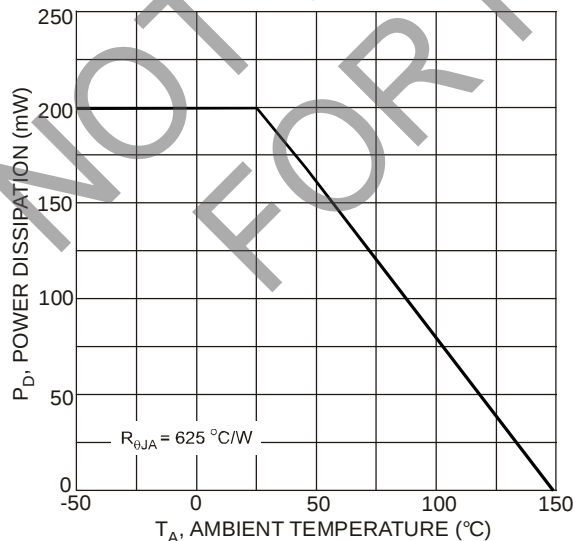
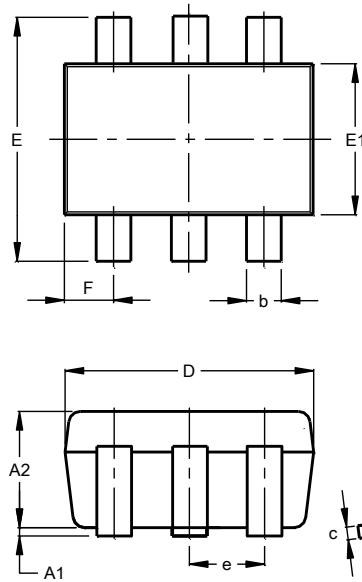


Fig. 11 Derating Curve - Total

Package Outline Dimensions

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

SOT363

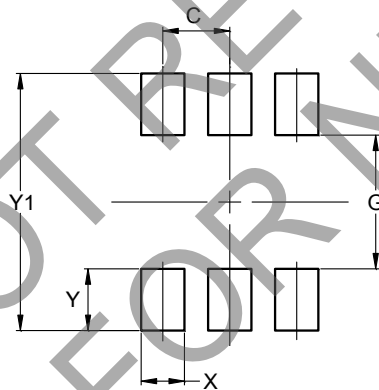


SOT363			
Dim	Min	Max	Typ
A1	0.00	0.10	0.05
A2	0.90	1.00	0.95
b	0.10	0.30	0.25
c	0.10	0.22	0.11
D	1.80	2.20	2.15
E	2.00	2.20	2.10
E1	1.15	1.35	1.30
e	0.650 BSC		
F	0.40	0.45	0.425
L	0.25	0.40	0.30
a	0°	8°	--
All Dimensions in mm			

Suggested Pad Layout

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

SOT363



Dimensions	Value (in mm)
C	0.650
G	1.300
X	0.420
Y	0.600
Y1	2.500

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