

Features

- Trench Power LV MOSFET Technology
- High Speed Switching
- High Density Cell Design for Low $R_{DS(ON)}$
- Halogen Free. "Green" Device (Note 1)
- Epoxy Meets UL 94 V-0 Flammability Rating
- Lead Free Finish/RoHS Compliant ("P" Suffix Designates RoHS Compliant. See Ordering Information)
- Moisture Sensitivity Level 1

Maximum Ratings

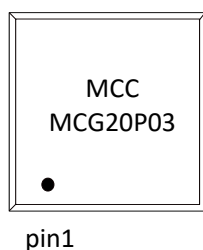
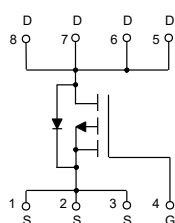
- Operating Junction Temperature Range : -55°C to +150°C
- Storage Temperature Range: -55°C to +150°C
- Thermal Resistance: 5.9°C/W Junction to Case⁽²⁾

Parameter		Symbol	Rating	Unit
Drain-Source Voltage		V _{DS}	-30	V
Gate-Source Volltage		V _{GS}	±20	V
Continuous Drain Current		I _D	-20	A
Pulsed Drain Current ⁽³⁾		I _{DM}	-80	A
Total Power Dissipation	T _C =25°C	P _D	21	W

Note:

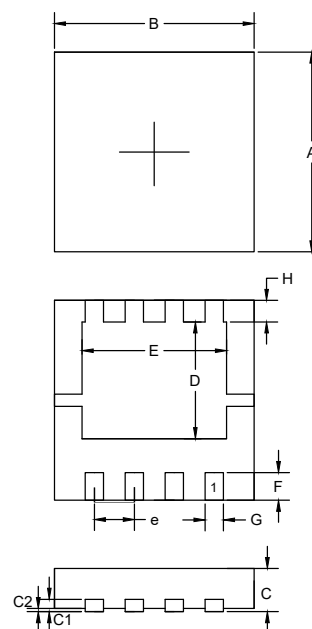
1. Halogen free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
2. $R_{\theta JA}$ is the sum of the junction-to-case and case-to-ambient thermal resistance, where the case thermal reference is defined as the solder mounting surface of the drain pins. $R_{\theta JC}$ is guaranteed by design, while $R_{\theta JA}$ is determined by the board design. The maximum rating presented here is based on mounting on a 1 in² pad of 2oz copper.
3. Pulse Test: Pulse Width ≤ 300us, Duty cycle ≤ 2%.

Internal Structure and Marking Code



P-CHANNEL MOSFET

DFN3333



DIMENSIONS					
DIM	INCHES		MM		NOTE
	MIN	MAX	MIN	MAX	
A	0.126	0.130	3.20	3.30	
B	0.126	0.130	3.20	3.30	
C	0.030	0.033	0.75	0.85	
C1	0.007	0.009	0.18	0.22	
C2	---	0.002	---	0.05	
D	0.071	0.079	1.80	2.00	
E	0.087	0.098	2.20	2.50	
F	0.016	0.020	0.40	0.50	
G	0.010	0.014	0.25	0.35	
H	0.012	0.016	0.30	0.40	
e	0.024	0.028	0.60	0.70	

Electrical Characteristics @ 25°C (Unless Otherwise Specified)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Static Characteristics						
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} =0V, I _D =-250μA	-30			V
Gate-Source Leakage Current	I _{GSS}	V _{DS} =0V, V _{GS} =±20V			±100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =-30V, V _{GS} =0V			-1	μA
Gate-Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =-250μA	-1	-1.5	-2.5	V
Drain-Source On-Resistance	R _{DS(on)}	V _{GS} =-10V, I _D =-10A		13.5	20	mΩ
		V _{GS} =-4.5V, I _D =-5A		20	27	mΩ
Gate Resistance	R _g	f=1 MHz, Open drain		7.5		Ω
Diode Characteristics						
Continuous Body Diode Current	I _S				-20	A
Diode Forward Voltage	V _{SD}	V _{GS} =0V, I _S =-20A			-1.2	V
Reverse Recovery Time	t _{rr}	I _F =-9A, dI _F /dt=500A/μs		14		ns
Reverse Recovery Charge	Q _{rr}			6		nC
Dynamic Characteristics						
Input Capacitance	C _{iss}	V _{DS} =-15V, V _{GS} =0V, f=1MHz		1750		pF
Output Capacitance	C _{oss}			220		
Reverse Transfer Capacitance	C _{rss}			185		
Total Gate Charge	Q _g	V _{DS} =-15V, V _{GS} =-10V, I _D =-9A		28.7		nC
Gate-Source Charge	Q _{gs}			5.5		
Gate-Drain Charge	Q _{gd}			5.4		
Turn-On Delay Time	t _{d(on)}	V _{DS} =-15V, V _{GEN} =-10V, R _G =2.5Ω, I _{DS} =-6A		10		ns
Turn-On Rise Time	t _r			44		
Turn-Off Delay Time	t _{d(off)}			54		
Turn-Off Fall Time	t _f			59		

Curve Characteristics

Fig. 1 - Typical Output Characteristics

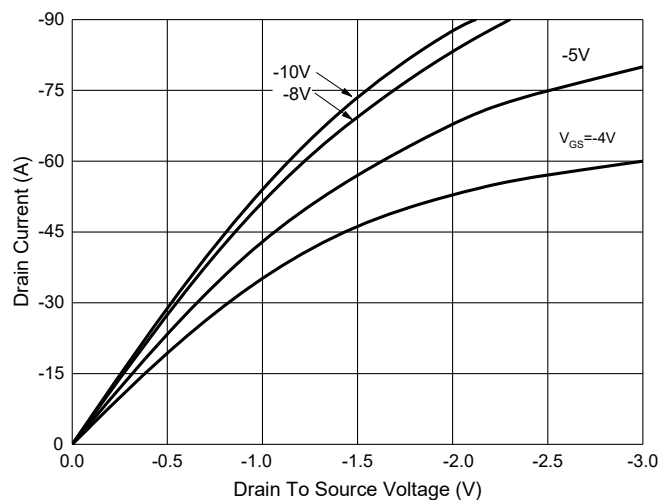


Fig. 2 - Transfer Characteristics

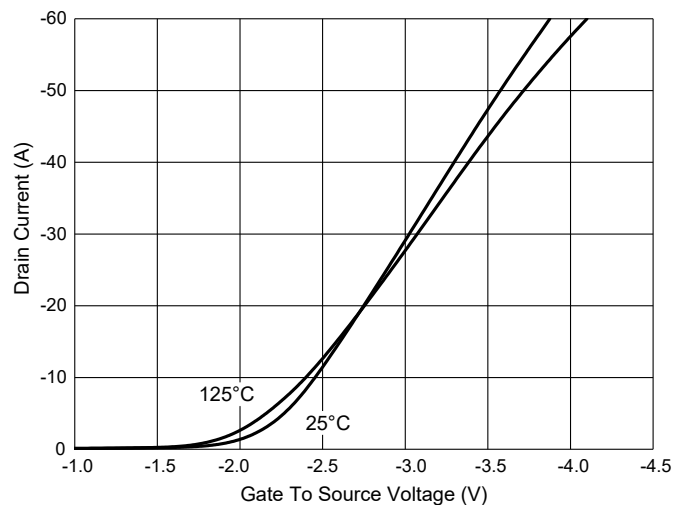


Fig. 3 - $R_{DS(ON)} - I_D$

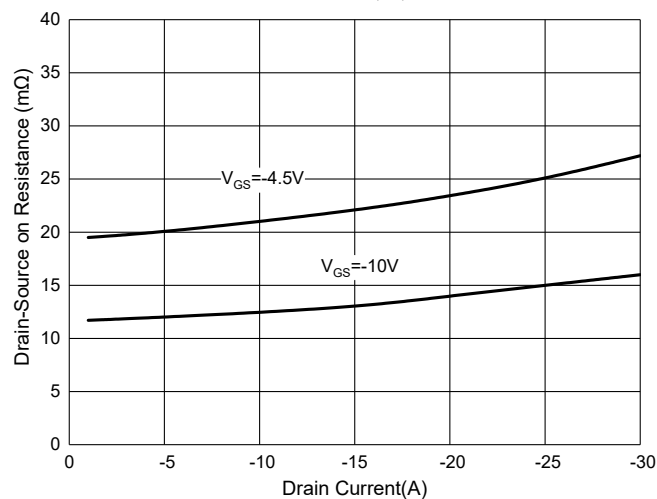


Fig. 4 - Normalized On Resistance Characteristics

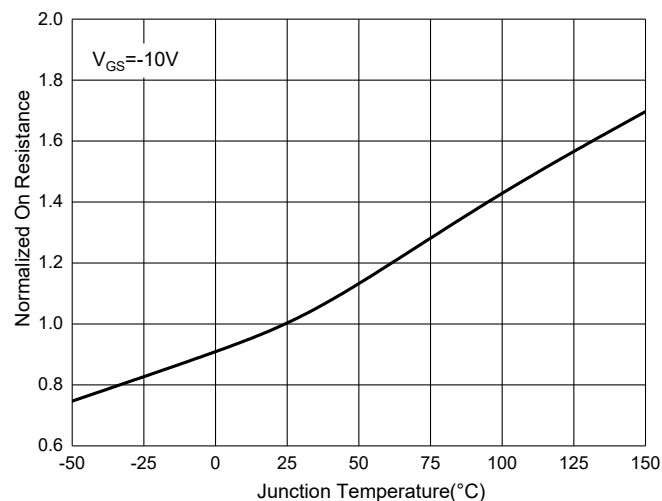


Fig. 5 - Capacitance Characteristics

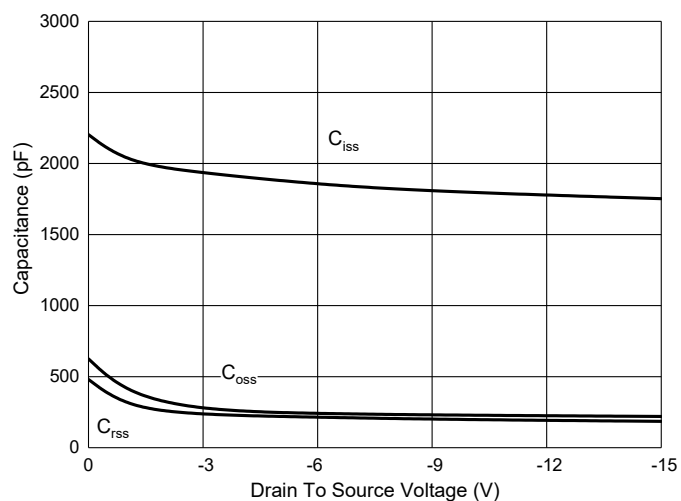
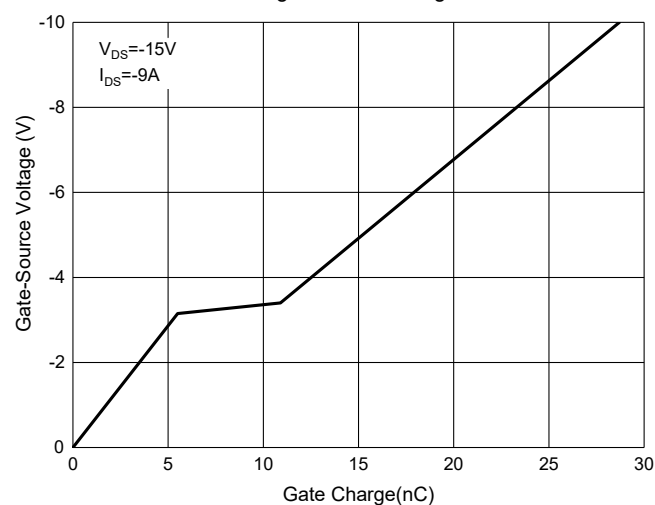


Fig. 6 - Gate Charge



Curve Characteristics

Fig. 7 - Safe Operation Area

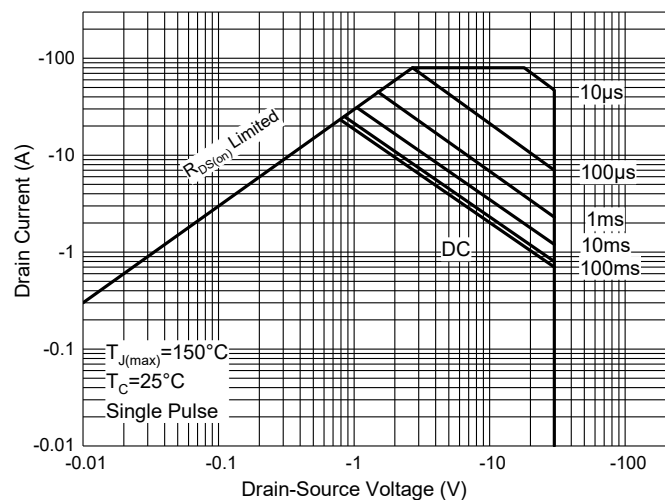
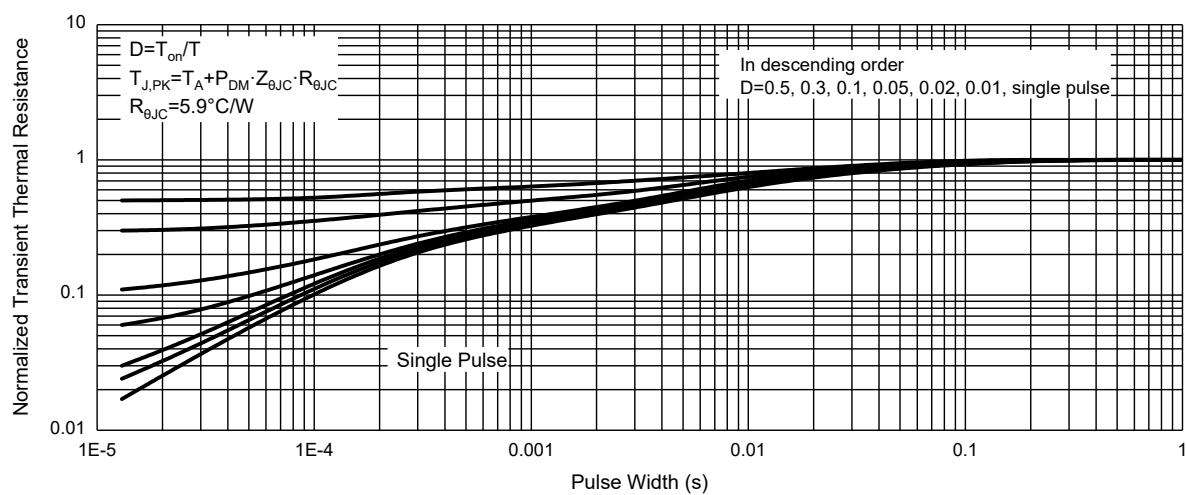


Fig. 8 - Normalized Transient Thermal Impedance



Ordering Information

Device	Packing
Part Number-TP	Tape&Reel: 5Kpcs/Reel

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